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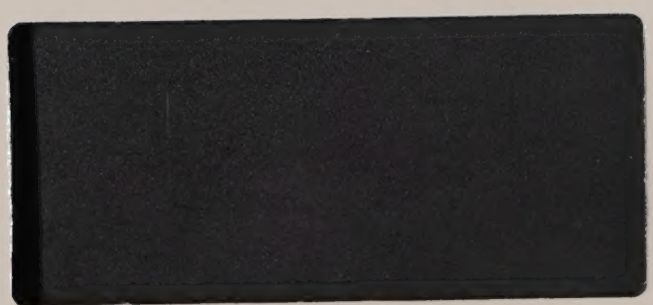

MODEL 1122B

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A UNIT OF
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Outside Mass., 1-800-TAU-TRON; Telex 750245

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1122B TRANSMISSION TEST SET AND 1122B-03 REMOTE DISPLAY SERVICE

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1. GENERAL

1.01 This section provides calibration procedures and troubleshooting aids for the TTI 1122B Transmission Test Set. Troubleshooting aids include a self-test routine description, a theory of operation description, and specifications.

1.02 Users' questions or comments regarding the scope, accuracy, or presentation of this section will be welcomed by the publisher. All will receive thorough evaluation. When appropriate, prompt replies will be made. Address correspondence to:

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10 Lyberty Way
Westford, MA 01886
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1.03 This section is published in looseleaf form to allow for insertion of changes made after printing. This method also allows insertion of

looseleaf note paper as a convenient means for containing users' notations. When change notices are received ***insert them immediately and record their presence in the change record below.***

CHANGE RECORD

THE FOLLOWING CHANGES ARE PRESENT IN THIS SECTION	
DESCRIPTION OR TITLE	**DATE OF INSERTION**

1.04 The 1122B Test Set (Fig. 1) measures transmission level, signal frequency and noise at voice-frequency test points in telephone transmission facilities. The test set includes a signal source to generate test tones. Two front-panel, 310-type jacks are used for input connections to the instrument measuring circuitry and output connections from the signal source. In addition to these front-panel jacks, a rear-panel terminal strip may be used for semi-permanent connection into the input and output circuits. A speaker allows audible monitoring of the received or transmitted signal. The test set is also equipped with a self-test feature. Self-contained microprocessors enable the instrument to test its major circuits and produce display messages indicating the result of each test.

2. CALIBRATION

A. General

2.01 This calibration procedure may be used as needed to restore the 1122B Test Set to operation within specifications or it may be used at regular intervals to verify operation within specifications. An out-of-calibration condition may be indicated if the 1122B test results vary from those of a calibrated instrument or if the self-test routine produces a FAIL message on either the log converter test or the analog circuit test. If the instrument cannot be calibrated to operate within specifications, troubleshoot for failure.

Note: The 1122B-03 Remote Display requires no calibration.

2.02 This calibration procedure assumes that the technician is familiar with the operating procedures of the instrument. These procedures are described in Section 1122-301.

2.03 The following equipment is recommended for performing this calibration procedure. Equivalent equipment may be used if necessary.

- Variac W5MT3A
- Dana 4700 Digital Multimeter (DMM)
- Tektronix 465 Oscilloscope
- Hewlett-Packard 745A AC Calibrator
- Itron 650 Counter-Timer
- General Radio Decade Attenuator
- Hewlett-Packard 333A Distortion Analyzer

B. Preliminary Procedures

2.04 To gain access to the instrument circuit board for calibration (Fig. 2):

(a) Switch the 1122B ON/OFF switch to OFF.

(b) Place the instrument upside down. Remove screws from bottom panel.

(c) Remove bottom panel.

Note: Calibration must be performed with instrument remaining upside down. Beware of making erroneous readings on the front panel displays due to this inverted position.

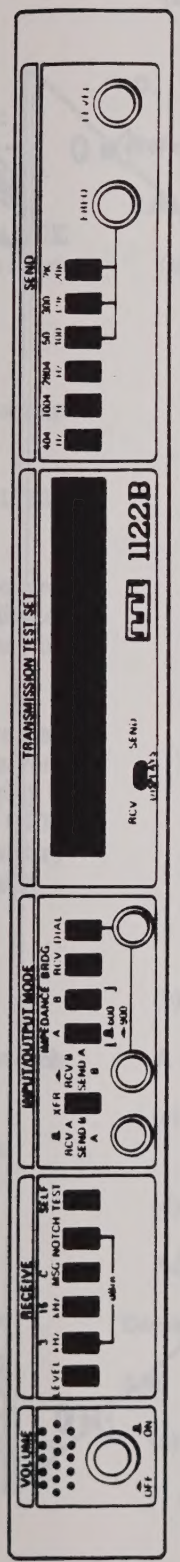


Fig. 1—1122B Transmission Test Set

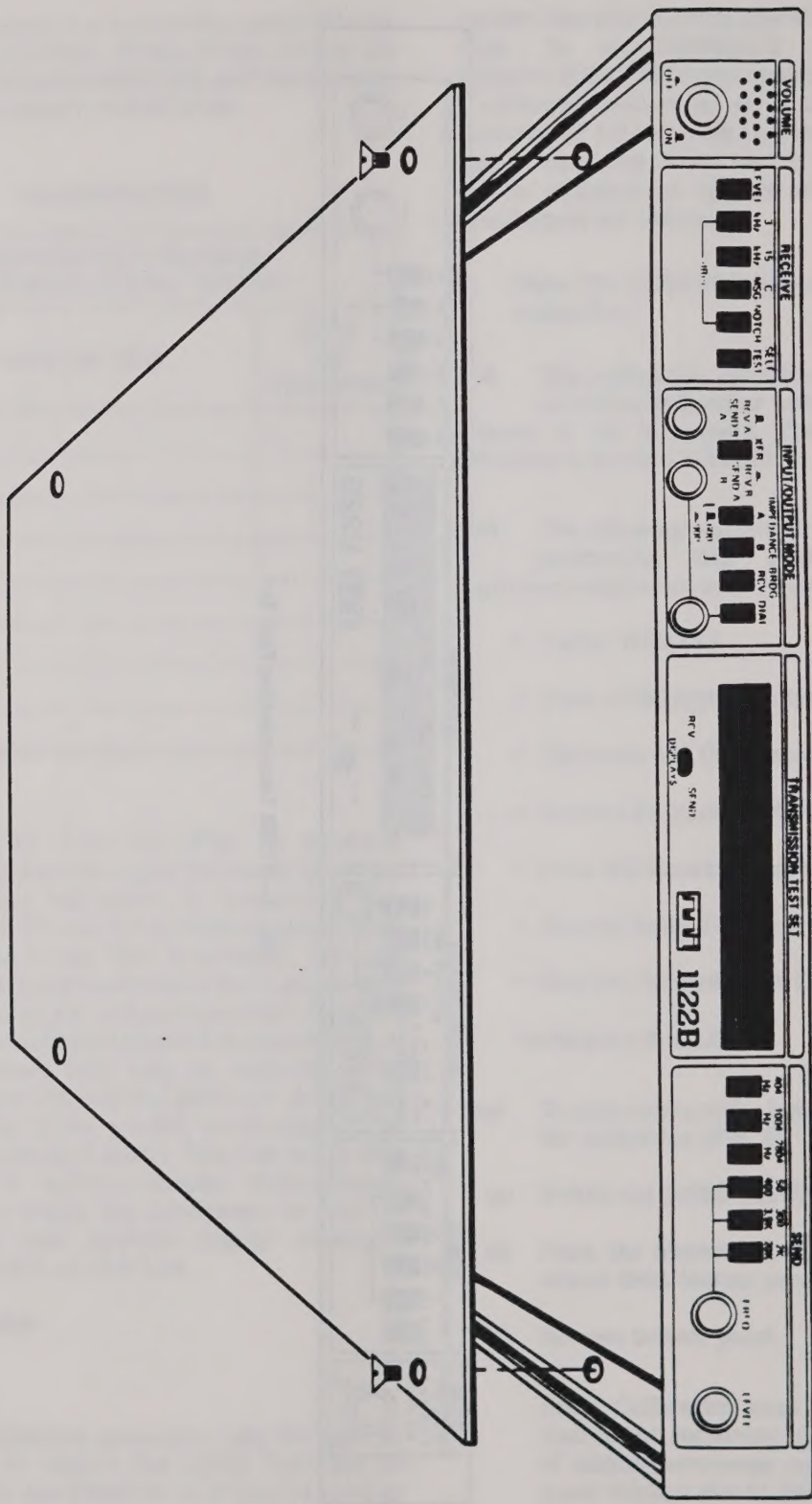


Fig. 2—Access to 1122B Main Circuit Board

C. AC Power Supply

Input Current

2.05 To measure input current requirement:

(a) Set up instruments as follows:

(1) Variac: ON/OFF switch to OFF, RANGE switch to 1A, voltage level control to maximum counterclockwise position.

(2) 1122B: ON/OFF switch to OFF.

(b) Plug 1122B ac power line into Variac output receptable.

(c) Set Variac ON/OFF switch to ON. Set 1122B ON/OFF switch to ON.

(d) Slowly turn Variac voltage level control clockwise until AC VOLTS meter indicates 120. Variac AC AMPERES meter should not indicate more than 0.1 A (100 ma).

Note: The 1122B requires a rapidly rising +5 V dc to start up properly. If the current drawn by the 1122B is normal but the display is not operating properly, turn the 1122B OFF, then back ON again. The test set circuitry resets, and the display should read normally.

Output Voltages

2.06 To test power supply output voltages:

(a) Set up DANA 4700 DMM: FUNCTION switch to DCV, RANGE to AUTO.

(b) Check for the following output voltages:

TP25	+15.0 ± 0.8 V dc
TP26	−15.0 ± 0.8 V dc
TP27	+ 5.0 + 0.3 V dc

Output Ripple

2.07 To check output ripple:

(a) Set up Tektronix Scope: VOLTS/DIV to 5m (5 mV/div), AC/GND/DC switch to AC, TIME/DIV to 1 ms.

(b) Check ripple at TP25, TP26 and TP27. Ripple at these points should not exceed 50 mV.

D. VF Circuit

Reference Voltages

2.08 To adjust reference voltages:

(a) Set up instruments as follows:

(1) HP Calibrator: VOLTAGE RANGE to 10 V, level switches to 489820, FREQUENCY RANGE to X100, FREQ Hz adjustment to 10, ERROR RANGE to 0, % ERROR adjustment to 0, LO output jack strapped to ground.

(2) Dana DMM: FUNCTION switch to DCV, RANGE switch to AUTO.

(3) Itron Counter: INPUT A connected to calibrator COUNTER OUTPUT, MODE switch to FREQUENCY, RANGE switch to 199 kHz, SENSITIVITY switch to MAX or AUTO, TIME BASE switch to INT, STORE switch to ON, SAMPLE RATE switch to MAX.

(4) 1122B: Select LEVEL switch in the RECEIVE section, disengage all other RECEIVE switches, RCV/SEND switch to RCV, XFR switch to RCV A/SEND B, A and B switches to 600 ohms.

(b) Connect a precision 600-ohms resistor (1% tolerance) in series with the 1122B A input/output jack and the calibrator output.

(c) Connect DMM to TP3 on the vf board. Adjust R158 on the vf board for +10.654 V dc.

(d) Set the DMM FUNCTION switch to ACV. Connect the DMM to TP11 on the vf board. Adjust R133 for +1.000 V ac.

Detector Balance

2.09 To balance detector circuit:

(a) Set up instruments as follows:

(1) Dana DMM: FUNCTION switch to DCV.

(2) HP Calibrator: VOLTAGE RANGE to 100 mV.

(3) Tektronix Scope: VOLTS/DIV to 1, AC/GND/DC switch to DC, TIME/DIV to 5 ms.

(b) Adjust R143 and R201 to the center of their ranges.

(c) Connect scope to pin 4 of U7 (Fig. 3). Adjust R143 until amplitude of the lower end of the waveform is consistent (Fig. 4).

(d) Connect DMM to TP6.

(e) Set calibrator VOLTAGE RANGE to 1 V. Adjust R173 for DMM reading of +10.000 V dc.

(f) Set calibrator VOLTAGE RANGE to 100 mV. Adjust R170 for DMM reading of +1.000 V dc.

(g) Repeat (e) and (f) until consistent readings are maintained when the calibrator VOLTAGE RANGE is switched from 100 mV to 1 V.

(h) If the specified readings are not achieved, readjust R143, then continue to repeat (e) and (f).

Note: The effects of R143, R173, and R170 interact. It may be necessary to repeat the procedure several times before the specified readings are obtained.

Log Converter Reference Levels

2.10 To adjust log converter:

(a) Set up HP Calibrator: VOLTAGE RANGE to 1 V, level switches to 489820, FREQUENCY RANGE to X100, FREQ Hz adjustment to 10, ERROR RANGE to X1, % ERROR to +0.5 %.

(b) Connect DMM to TP6. DMM should read 10 V. If not, range up to 10 V on the calibrator, then back to 1 V.

(c) Adjust R158 until 1122B level display flashes between −10.0 and −10.1 dBm. This approximates a reading of −10.05 dBm.

(d) Set calibrator VOLTAGE RANGE to 100 mV. Level display should flash between −30.0 and −30.1 dBm. If not, adjust R175. DMM should read 1 V.

(e) Repeat (c) and (d) until consistent readings of −10.05 and −30.05 dBm are maintained when the calibrator is switched from 100 mV to 1 V.

2.11 To verify log converter adjustment:

(a) Set up HP Calibrator: VOLTAGE RANGE to 10 V, level switches to 154920, % ERROR to 0.

(b) Level display should read 0.0 dBm.

(c) Vary calibrator % ERROR in the negative direction until display flashes between 0.0 and 0.1 dBm. This should occur between −0.5% and −0.8% on the calibrator.

(d) Vary the calibrator % ERROR in the positive direction until display flashes between 0.0 and −0.1 dBm. This should occur between 0.5% and 0.8% on the calibrator.

Level-Measurement Auto-Ranging

2.12 To check level auto-ranging:

(a) Set up instruments as follows:

(1) HP Calibrator: VOLTAGE RANGE TO 10 V, level switches to 275490, FREQUENCY RANGE TO X100, FREQ Hz adjustment to 10, ERROR RANGE to 0, % ERROR adjustment to 0.

(2) GR Decade Attenuator: 10 dB/STEP to 0, 1 dB/STEP to 0, 0.1 dB/STEP to 0.

(b) Connect calibrator output to attenuator input in series through a 600-ohm resistor. Connect attenuator output to 1122B A input/output jack.

(c) Use the 10 dB/STEP switch on the attenuator to range down. Observe the following level display readings on the 1122B for each 10 dB step.

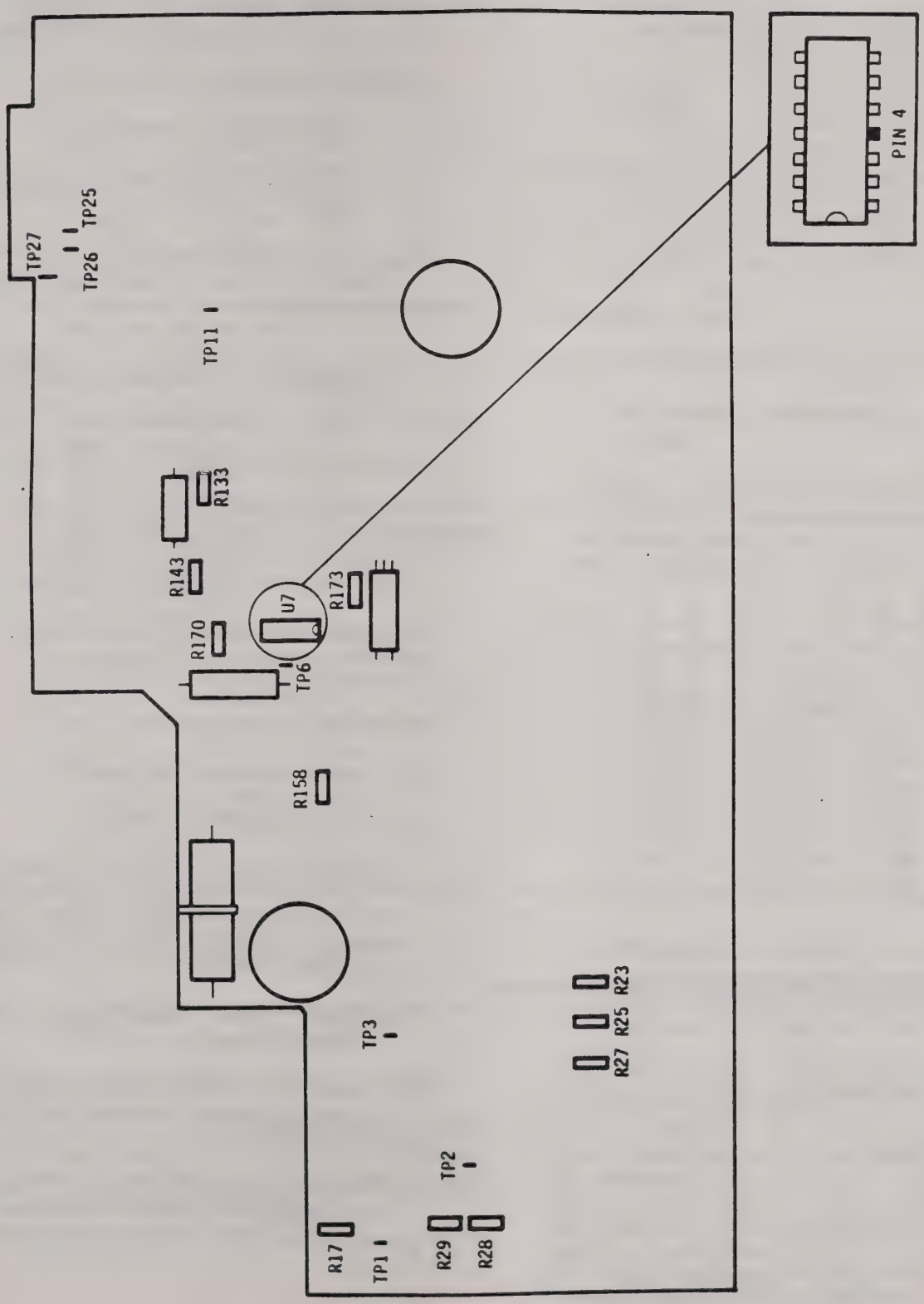
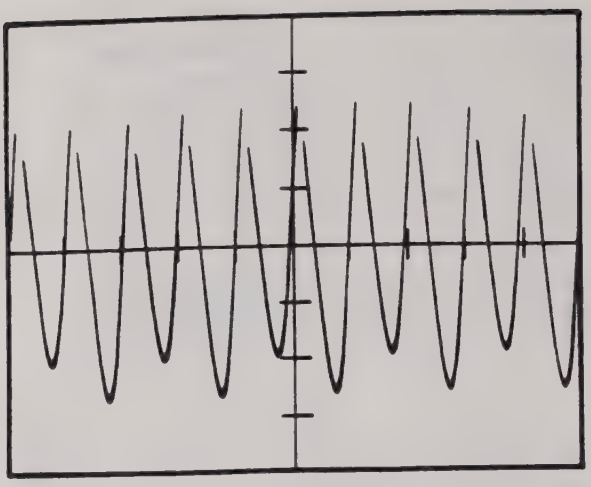
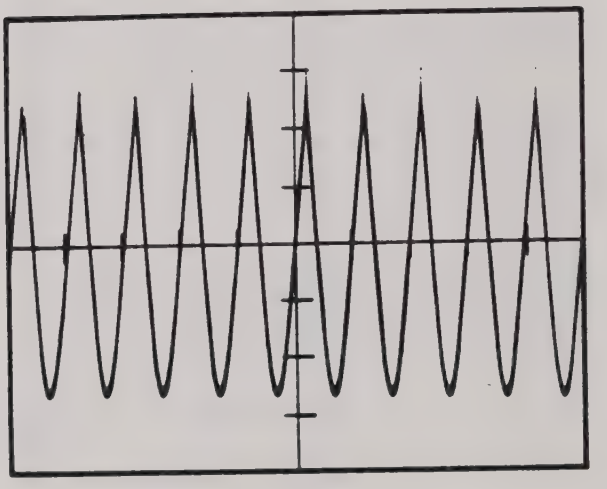


Fig. 3—Locating Test Points and Adjustments on the Main Circuit Board



INCONSISTANT LOWER AMPLITUDE



CONSISTANT LOWER AMPLITUDE

Fig. 4—Typical Waveforms at Pin 4 of U7 of VF Circuit

ATTENUATOR	DISPLAY READING
0	5.0 dBm ± 0.1 dB
1	− 5.0 dBm ± 0.1 dB
2	−15.0 dBm ± 0.1 dB
3	−25.0 dBm ± 0.1 dB
4	−35.0 dBm ± 0.1 dB
5	−45.0 dBm ± 0.1 dB
6	Ur (under range)

Noise Measurement Auto-Ranging

2.13 To check noise measurement auto-ranging:

(a) Set up instruments as follows:

(1) GR Attenuator: 10 dB/STEP to 0.

(2) 1122B: Select 15 kHz switch in RECEIVE section.

(b) Use 10 dB/STEP switch to range down. Observe the following noise display readings for each 10 dB step.

ATTENUATOR	DISPLAY READING
0	95 dBrn ± 1.0 dB
1	85 dBrn ± 1.0 dB
2	75 dBrn ± 1.0 dB
3	65 dBrn ± 1.0 dB
4	55 dBrn ± 1.0 dB
5	45 dBrn ± 1.0 dB
6	35 dBrn ± 1.0 dB
7	25 dBrn ± 1.0 dB
8	15 dBrn ± 1.0 dB

E. Filter Board

3-kHz Flat Noise Weighting Network

2.14 To check noise weighting network:

(a) Set up instruments as follows:

(1) HP Calibrator: VOLTAGE RANGE to 10 V, level switches to 154920, FREQUENCY RANGE to X100, FREQ Hz adjustment to 10, ERROR RANGE to X1, % ERROR adjustment to 0.

(2) 1122B: Select 3-kHz switch in the RECEIVE section.

(b) Connect a precision 600-ohm resistor (1% tolerance) in series with the calibrator output and the 1122B A jack.

(c) Vary the calibrator FREQUENCY RANGE and FREQ Hz adjustment to obtain the frequencies listed below. Observe the 1120 noise

display for the readings designated for each frequency.

FREQUENCY	LEVEL DISPLAY READING
60 Hz	95 dBrn ± 1 dB
400 Hz	90 dBrn ± 1 dB
1 kHz	90 dBrn Reference
2 kHz	89 dBrn ± 1 dB
3 kHz	87 dBrn ± 1 dB
6 kHz	78 dBrn ± 1 dB

(d) Continue to increase frequency until display reads less than 30 dBrn.

15-kHz Flat Noise Weighting Network

2.15 To check 15-kHz noise weighting network:

(a) Set up the 1122B: Select 15-kHz switch.

(b) Vary calibrator FREQUENCY RANGE and FREQ Hz adjustment for the frequencies listed below. Observe the 1122B display for the readings designated for each frequency.

FREQUENCY	LEVEL DISPLAY READING
60 Hz	90 dBrn ± 1 dB
400 Hz	90 dBrn ± 1 dB
1 kHz	90 dBrn Reference
10 kHz	89 dBrn ± 1 dB
15 kHz	87 dBrn ± 1 dB
30 kHz	78 dBrn ± 1 dB

Note: Frequencies above 20 kHz will not appear on the 1122B display.

C-Message Noise Weighting Network

2.16 To check C-message noise weighting network:

(a) Set up the 1122B: Select CMSG switch.

(b) Vary the calibrator FREQUENCY RANGE and FREQ Hz to obtain the frequencies below. Observe the 1122B display for the readings designated for each frequency.

FREQUENCY	DISPLAY READING
60 Hz	34 dBrn ± 2 dB
300 Hz	73 dBrn ± 1 dB
700 Hz	87 dBrn ± 1 dB
1.0 kHz	90 dBrn Reference
2.0 kHz	89 dBrn ± 1 dB
3.3 kHz	85 dBrn ± 2 dB
5.0 kHz	62 dBrn ± 3 dB
15.0 kHz	30 dBrn Maximum

Notched Noise Filter

2.17 To check notched noise filter:

(a) Set up the 1122B: Select 15 kHz and NOTCH switches.

(b) Vary the calibrator FREQUENCY RANGE and FREQ Hz adjustment for the frequencies listed below. Observe the 1122B display for the reading designated for each frequency.

FREQUENCY	DISPLAY READING
300 Hz	90 dBrn ± 1 dB
400 Hz	90 dBrn ± 1 dB
995 Hz	40 dBrn Maximum
862 Hz	87 dBrn ± 1 dB
1010 Hz	30 dBrn Maximum
1025 Hz	40 dBrn Maximum
1182 Hz	87 dBrn ± 1 dB
1700 Hz	90 dBrn ± 1 dB
3000 Hz	90 dBrn ± 1 dB

Note: Readings at 995 and 1025 should be approximately the same, since the notch at 1010 should be symmetrical. This symmetry is not critical, however, to operation within specifications.

Level-Measurement Balance

2.18 To check balance of level-measurement circuitry:

(a) Set up the 1122B: Select LEVEL switch.

(b) Display should read 0.0 dBm. Reverse tip and ring. Display should read 0.0 dBm.

(c) Remove the 600-ohm resistor from the input of the 1120. Select the A RCV BRDG mode with the 1122B INPUT/OUTPUT MODE switch. Display should read 6.0 dBm ± 0.3 dB.

(d) Reverse tip and ring. Display should read 6.0 dBm ± 0.3 dB.

(e) Set calibrator level switches to 189100. Connect a 900-ohm resistor in series with output of the calibrator and the A input/output jack of the 1122B test set. Using the 1122B A switch, select 900-ohm termination.

(f) Display should read 0.0 dBm ± 0.3 dB. Reverse tip and ring. Display should read 0.0 dBm ± 0.3 dB.

(g) Repeat (b) through (f) on 1122B B jack.

F. Oscillator

Reference Voltages

2.19 To adjust oscillator reference voltages:

(a) Set up instruments as follows:

(1) Dana DMM: FUNCTION switch to DCV, RANGE switch to AUTO.

(2) Tektronix Scope: VOLTS/DIV to 1, AC/GND/DC switch to AC, TIME/DIV to 5 ms.

(b) Connect DMM to TP1. Adjust R17 for a reading of 10 V dc.

(c) Adjust R28 and R29 to the center of their ranges.

(d) Connect scope to TP2. Select 1004 Hz switch on 1122B SEND section.

(e) Adjust R28 for symmetrical waveform (see Fig. 5). Adjust R29 sine wave (see Fig. 6).

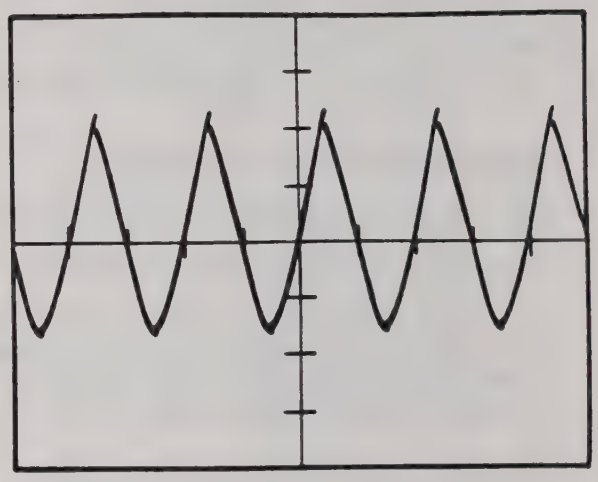
(f) Continue to adjust R28 and R29 until proper waveform is achieved.

Waveform Symmetry

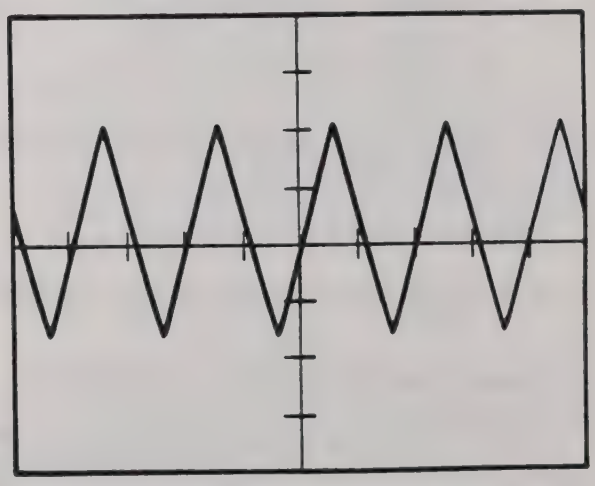
2.20 To adjust oscillator waveform symmetry:

(a) Set up HP Distortion Analyzer: FUNCTION switch to VOLTMETER, adjust meter to read 0 V on 1 V scale, FUNCTION switch to SET LEVEL, METER RANGE switch to 0, SENSITIVITY switch to 0, FUNCTION switch to DISTORTION, terminate meter in 600 ohms.

(b) Use the frequency control and BALANCE control to adjust the distortion analyzer meter for minimum distortion.



ASYMMETRICAL WAVEFORM



SYMMETRICAL WAVEFORM

Fig. 5—Typical Waveforms at TP2 of Oscillator

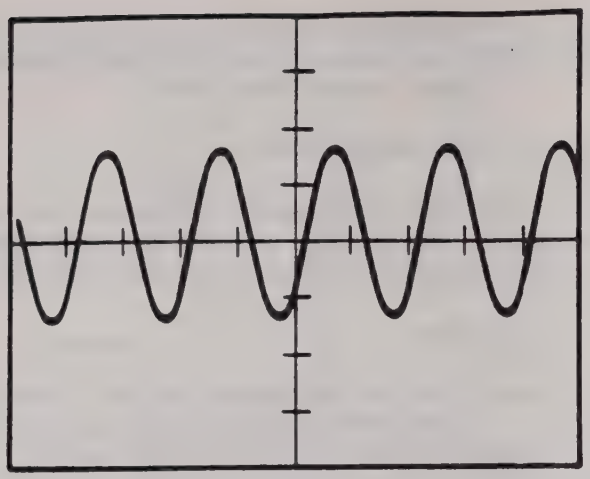


Fig. 6—Typical Sine Wave at TP2 of Oscillator

(c) Adjust R28 and R29 for minimum distortion reading on the analyzer.

Reference Frequencies

2.21 To adjust oscillator reference frequencies:

(a) Set up the 1122B: Select SEND on the RCV/SEND switch. Select 300-3.9K switch in the SEND section. Set FREQ control to the maximum counterclockwise position.

(b) Adjust R17 for a reading of 270 Hz on the 1122B frequency display.

(c) Select 404 Hz switch. Adjust R23 for a reading of 404 Hz on the display.

(d) Select 1004 Hz switch. Adjust R25 for a reading of 1004 Hz on the display.

(e) Select 2804 Hz switch. Adjust R27 for a reading of 2804 Hz on the display.

Reference Level Adjust

2.22 To adjust oscillator reference level:

(a) Set up the 1122B: RCV/SEND switch to SEND, select 1004 switch, set LEVEL control for 0.0 dBm reading on display, XFR switch to RCV A/SEND B, A and B switches to 600 ohms.

(b) Use a patch cord to connect the A jack to the B jack.

(c) Set RCV/SEND switch to RCV position. Adjust R51 for a reading of 0.0 dBm on the level display.

(d) Set RCV/SEND switch to SEND position. Set LEVEL control to the maximum counterclockwise position. Vary LEVEL control through its full range to verify output levels between −40 dBm through +10 dBm.

(e) Select 300-3.9K switch. Adjust FREQ control for reading of 1004 Hz on the frequency display.

(f) Set RCV/SEND switch to RCV. Select 15 kHz switch in the RECEIVE section. Adjust LEVEL control for a reading of 90 dBrn on the display.

(g) Select NOTCH switch in the RECEIVE section. Select 1004 Hz switch in the SEND section. Display should read 30 dBrn or less. If not, re-adjust for minimum distortion as described in 2.20.

Frequency Ranges

2.23 To check the frequency ranges:

(a) Select 50-400 Hz switch. Vary FREQ control through its full range to verify frequencies between 50 and 400 Hz.

(b) Select 300-3.9k Hz switch. Vary FREQ control through its full range to verify frequencies between 300 Hz and 3.9 kHz.

(c) Select 2K-20K Hz switch. Vary FREQ control through its full range to verify frequencies between 2 kHz and 20 kHz.

(d) Select 1004 Hz switch in the SEND section. Use LEVEL control to set level reading to 0.0 dBm.

(e) Select 2K-20K Hz switch. Use FREQ control to set frequency reading to 20 kHz. Level reading should not exceed 0.5 dBm.

(f) Select LEVEL switch in the RECEIVE section. Set RCV/SEND switch to RCV. Level reading should not exceed 0.5 dBm.

Harmonic Distortion

2.24 To verify harmonic distortion values:

(a) Distortion Analyzer: FUNCTION switch to SET LEVEL, METER RANGE switch to 0 dBm.

(b) Connect analyzer to the 1122B B jack.

(c) Use the 1122B frequency range switches and the FREQ and LEVEL controls to achieve display readings listed below. Tune distortion analyzer for minimum distortion. Observe that distortion does not exceed the values listed below for each 1122B oscillator output.

1122B OUTPUT			MAXIMUM DISTORTION
Range	FREQ	LEVEL	
50-400	100 Hz	0.0 dBm	−40 dB
	400 Hz	0.0 dBm	−50 dB
300-3.9K	300 Hz	0.0 dBm	−50 dB
	900 Hz	0.0 dBm	−50 dB
2K-20K	2 kHz	0.0 dBm	−50 dB
	20 kHz	0.0 dBm	−40 dB

3. FAILURE ISOLATION

3.01 This Part provides information to aid in isolating failure to the major assemblies or circuits of the 1122B test set and the remote display.

A. Fuses

3.02 The test set requires one 1/4 A, slow-blow fuse which is located on the rear panel. To check or replace fuse, push fuseholder and turn in the direction of the arrow marked on the holder.

3.03 The remote display requires one 1/4 A, fast-blow fuse. To check or replace fuse:

(a) Remove the screws from the rear panel of the display housing. Remove the rear panel.

(b) Remove the screws fastening the circuit board to the housing. Fuse is located on the component side of the circuit board.

B. Display/Instrument Failure Isolation

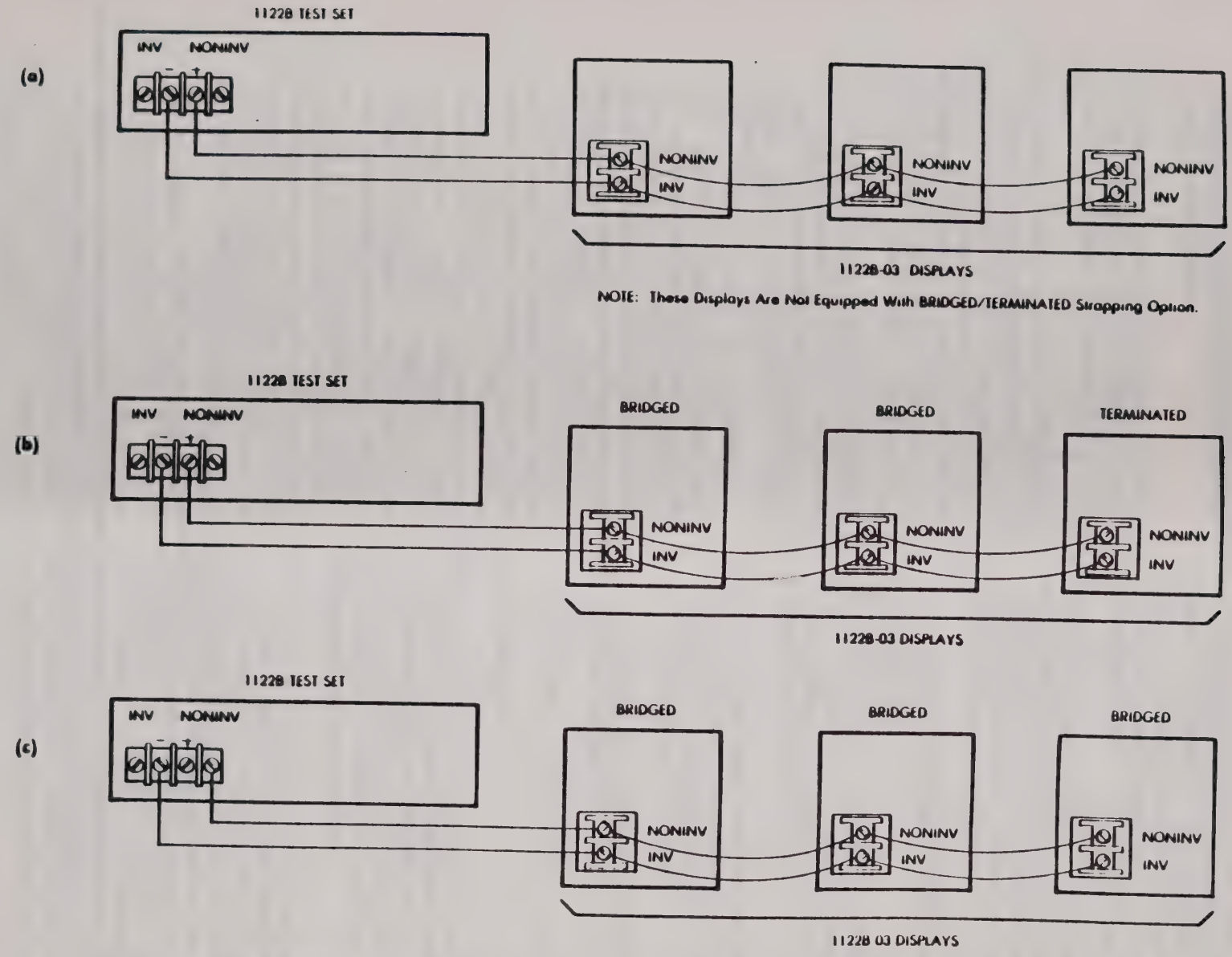
3.04 When trouble indications appear on the remote display, the failure must be isolated to either the display or the test set.

TABLE A
DISPLAY CONNECTION PROCEDURE

1122B TEST SET SERIAL NUMBER	1122B-03 DISPLAY SERIAL NUMBER	DISPLAY CONNECTION PROCEDURE
236 - 364	101 - 184	A
	185 and above	C
365 and above	101 - 184	NOTE 2
	185 and above	B

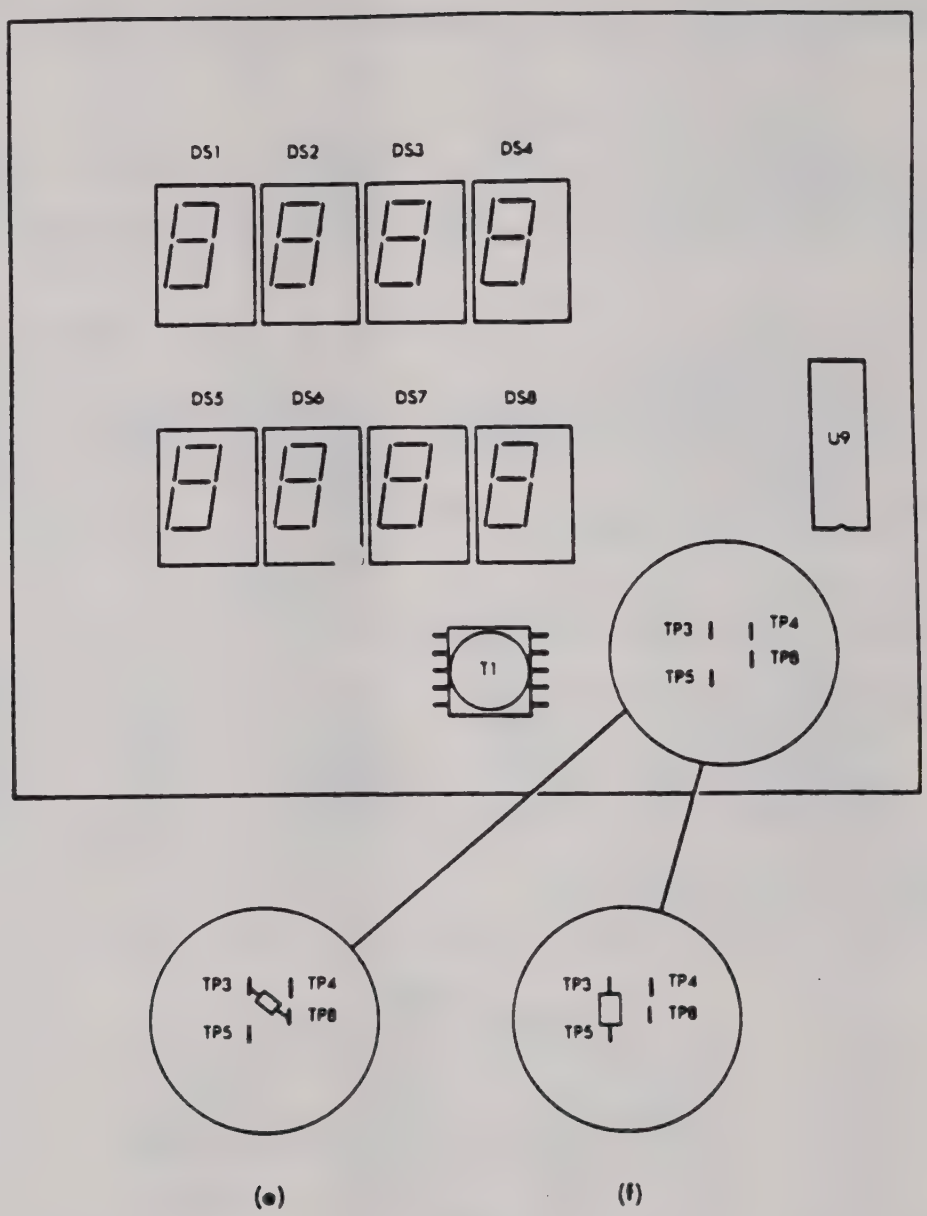
NOTES:

1. Displays with serial number 185 and above must not be intermixed with displays with serial numbers 101 - 184.
2. These test sets and displays cannot be connected.



NOTE: These Displays Are Not Equipped With BRIDGED/TERMINATED Strapping Option.

Fig. 7—Display Connection. (a) For Procedure A, (b) for Procedure B, and (c) for Procedure C. (Sheet 1 of 2).



(d)

(e)

(f)

Note: Displays with serial number 600 and above have a bridge-terminate switch on back of the unit. No internal strapping is required.

Fig. 7—Display Connection. (d) Location of TP3, TP5, and TP8 for strapping option, (e) location of resistor for TERMINATED display, and (f) location of resistor for BRIDGED display. (Sheet 2 of 2)

Note: Remove power from test set and displays before working on connections or internal strapping. To isolate failure:

(a) Check the output from the test set with a scope. There should be a binary serial output present. If not, failure is in the test set.

(b) Check the display connections and strapping. To do this, examine the manufacturer's label affixed to the rear panels of the test set and displays. Note the serial number of each, then refer to Table A. To determine which connection and strapping option procedure should have been used. The procedures are as follows:

(1) ***Procedure A (Fig. 7a)***: Connect NONINV test set terminal to NONINV display terminal. Connect INV test set terminal to INV display terminal. Connect subsequent displays in parallel.

(2) ***Procedure B (Fig. 7b)***: Each display has a label on the rear panel describing which strapping option is in place. The final display should be strapped and marked TERMINATED and all others should be strapped and marked BRIDGED. (Displays received from the manufacturer are TERMINATED and so marked.) To change the strapping option, remove power from display. Remove front panel and display. Locate terminals TP3, TP5, and TP8 at the lower right corner of board (Fig. 7d). Remove the 1 kohm terminating resistor connected between two of these points. For a TERMINATED display, reinstall the 1 kohm terminating resistor between TP3 and TP8 (Fig. 7e). For a BRIDGED display, reinstall the 1 kohm resistor between TP3 and TP5 (Fig. 7f). Mark strapping option on label on rear panel. Replace front cover. Connect display(s) as in Procedure A, using BRIDGED displays for all but the final TERMINATED display.

(3) ***Procedure C (Fig. 7c)***: Check rear panel of display(s) to determine that all are BRIDGED. If any are TERMINATED, modify them as explained in Procedure B above. Connect displays as shown in Fig. 7c. Note that the unmarked terminal on the display output terminal block connects to the NONINV display terminal and acts as a ground lug.

(c) Correct any improper connections and strapping. If one display still malfunctions when power is re-applied, remove the display from the line-up and arrange for its return to the manufacturer for repair. Reconnect and if necessary change strapping on the remaining displays.

(d) If more than one display malfunctions when power is re-applied, one or more displays may be defective. A defective display can cause good displays to malfunction. To determine by elimination which displays are defective:

(1) Remove signal leads from first display and test set.

(2) Disconnect a malfunctioning display from the line-up. If Procedure B (above) was used to connect displays, strap the malfunctioning display TERMINATED.

(3) Connect signal leads from the test set to the display. If the display still malfunctions when power is applied, arrange for its return to the manufacturer for repair.

(4) Repeat steps (2) and (3) for all malfunctioning displays.

(5) Restrap and reconnect good displays.

C. Self-Testing

3.05 The remote display is able to self-test its display segments. When power is applied to the display, all the display segments flash on, showing all eights and decimal points. This allows the operator or maintenance technician to ascertain that all display segments are operating.

3.06 The instrument's self-test routine checks the major circuits of the test set and produces display messages indicating the results of each test. These display messages are listed and described in Table B. A PASS message indicates proper operation of the circuit tested. A FAIL message indicates the need for calibration or repair. The possible sources of failure associated with each FAIL message and the appropriate corrective action are given in Table B. Note that a FAIL message on either the log converter test or the analog circuit test may indicate an out-of-calibration condition and not necessarily the need for repair.

TABLE B
SELF TEST ROUTINE DISPLAY MESSAGES

TEST	DISPLAY MESSAGE, INDICATION	POSSIBLE SOURCE OF FAILURE	CORRECTIVE ACTION
Display	All segments fail to light	Fuse Power Supply Microprocessor/Digital	1. Replace blown fuse. 2. Check cable between display. 3. Check cable between main board power supply. 4. Troubleshoot for failure.
	Some segments, numerals, or decimals fail to light.	Segment(s) Display Drivers(s)	Troubleshoot for failure.
	All segments shown 9 and decimal		None
Log Converter	FAIL LC	VF Circuit	1. Calibrate. 2. Troubleshoot for failure.
	PASS LC		None
Instrument Setup	SEND OFF	XFR Switch set to SEND	Set XFR switch to RCV.
	PUSH LEVL	LEVEL switch not engaged	Push LEVL switch.
	PUSH 1004	1004 switch not engaged	Push 1004 Hz switch.
Analog Circuit	FAIL ANLG	Oscillator Filters VF Circuit	1. Set XFR switch to SEND; if no tone, troubleshoot for failure. 2. Calibrate. 3. Troubleshoot for failure.
	PASS ANLG		None
Digital Circuit	FAIL d	Microprocessor/Digital	Troubleshoot for failure.
	PASS d		None

4. THEORY OF OPERATION

A. Input/Output Connections

4.01 Two sets of input/output jacks designated A and B are provided for signal input and oscillator output. In addition to these front-panel jacks, a rear-panel terminal strip may be used for semi-permanent connection into the test set input/output circuits. The rear-panel terminals are designated tip, ring, and sleeve for both the A and the B input/output circuits. These terminals are automatically disconnected from their associated circuits when a plug is inserted into the jack (Fig. 8). A front-panel switch controls transmit and receive directions for the A and B circuits. Either circuit may be used for transmitting or receiving, allowing the test set to simultaneously make measurements over one circuit and send tone over the other.

4.02 The tip and ring leads of the A and B circuits are balanced to ground. The sleeve leads are connect to chassis ground, which in turn, is connected to earth ground through the ac power cord. Both input/output circuits are capacitor-transformer coupled to pass ac and block dc voltages. When a terminated mode is selected, a resistor shunts the transformer primary winding to provide a matched the bridged mode is selected, an input/output impedance of over 20 k ohms is provided by the transformer.

B. Filters (Fig. 9)

4.03 In addition to the level/frequency measurement mode, the 1122B has two noise-measurement modes: idle-channel noise and notched-noise. Idle-channel noise is measured by selecting one of the three noise weighting networks which is then switched into the measuring circuit. These weighting networks are: 3-kHz flat, 15-kHz flat, and C-message. When notched-noise is selected, a 1010-Hz notch filter is switched into the circuit. The notch filter is used in conjunction with one of the three weighting networks.

C. VF Circuit

4.04 The 1122B uses a common circuit to measure both noise and level. Since the circuit has a 20-dB measuring range, it contains a microprocessor controlled auto-range network to range the input signal to within the circuit's measuring capability. This is accomplished by FET switches which cover the 100-dB dynamic range of the test set in 20-dB steps. From the auto-range network, the signal is fed to the detector.

4.05 The true-rms detector produces a dc output proportional to the rms value of the auto-ranged ac signal. This dc signal is then changed to logarithmic form in the log converter. A capacitor in the log converter charges to the reference voltage, then discharges through a resistor. The time required for the capacitor to discharge to the level of the detected voltage is proportional to the logarithm of the detected voltage. The output of the log converter is applied to the microprocessor.

D. Microprocessor

4.06 The microprocessor monitors the status of the front-panel switches and controls the operation of the entire test set. It is supported by an EPROM (erasable programmable read-only memory), counters to perform frequency measurement, and the display drivers.

4.07 The microprocessor receives information from the auto-range network and, in turn, controls the auto-ranging function. It also receives the output of the log converter. When the level-measurement mode is selected, the microprocessor converts the log converter output from a true-rms value to an averaged value, as is customary in telephone practice. The microprocessor analyzes the averaged value to control the noise/level display. These drivers operate the individual segments of the noise/level display. When noise measurement is selected, the microprocessor analyzes the true-rms value from the log converter to control the noise/level display drivers.

4.08 Frequency is measured whenever the level or a noise measurement mode is selected. The frequency measurement circuit consists of a frequency counter, logic, and a binary counter. This circuit is also microprocessor controlled. The output of the frequency measuring circuit is routed to the frequency display drivers which operate on command of the microprocessor.

E. Oscillator

4.09 The signal generator is a voltage-controlled oscillator with automatic gain control for amplitude stability. The basic oscillator frequency is controlled by a resistive voltage divider which provides

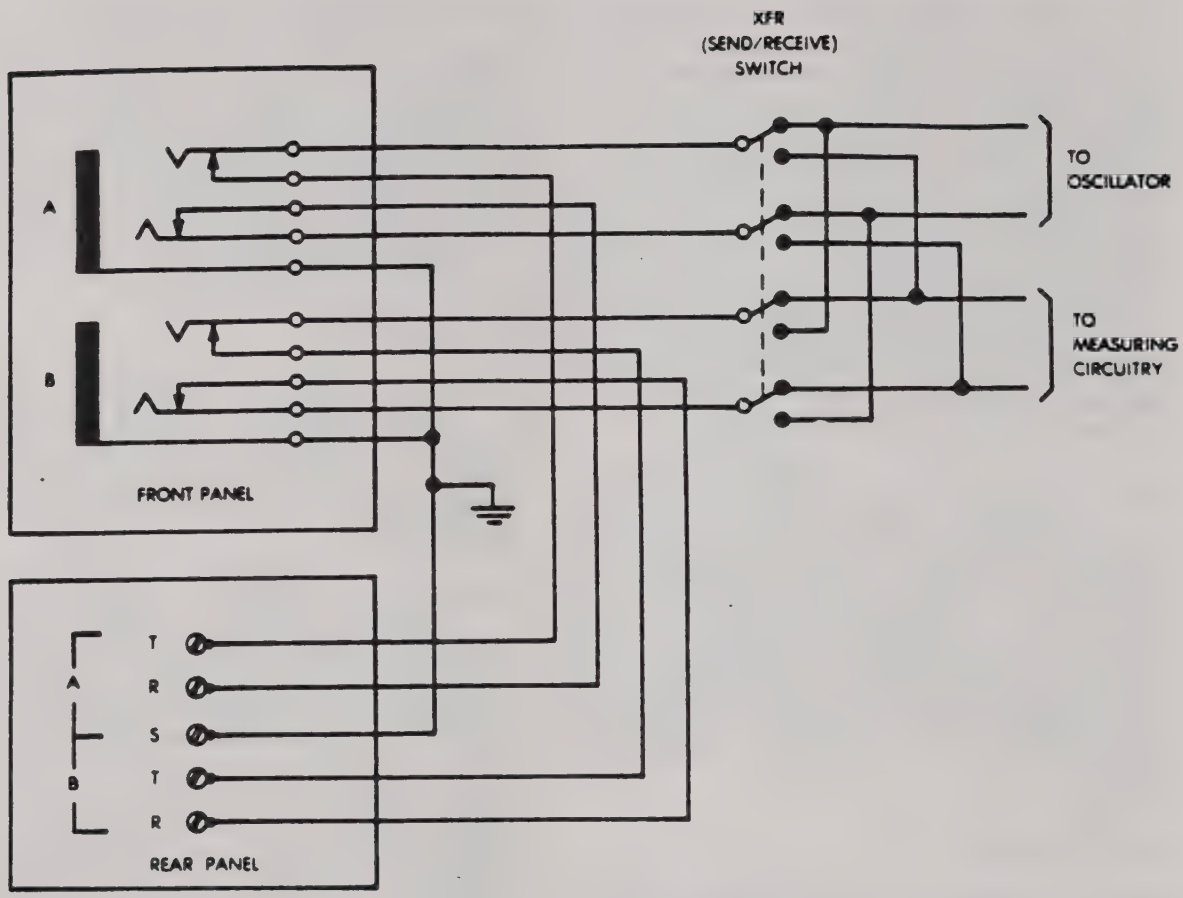


Fig. 8—Input/Output Circuits

selection of three fixed frequencies (404 Hz, 1004 Hz, and 2804 Hz), as well as continuous tuning within each of three overlapping frequency bands (50 Hz to 400 Hz, 300 Hz to 3.9 kHz, and 2 kHz to 20 kHz).

4.10 The fixed-level output of the oscillator is applied to a continuously variable attenuator for output level adjustment. The signal from the attenuator goes to the vf circuit and to the output stage of the signal generator. The frequency and level of the oscillator signal is measured in the test set measuring circuits and then monitored on the displays and speaker. The constant-current output stage is buffered and transformer coupled to the A or B input/output circuit.

F. Remote Display

4.11 The microprocessor in the test set converts the measurements of the transmitted or received signal into a binary serial output. This data is transmitted to the 1122B-03 remote display which translates it into numerical readouts of frequency and noise or level.

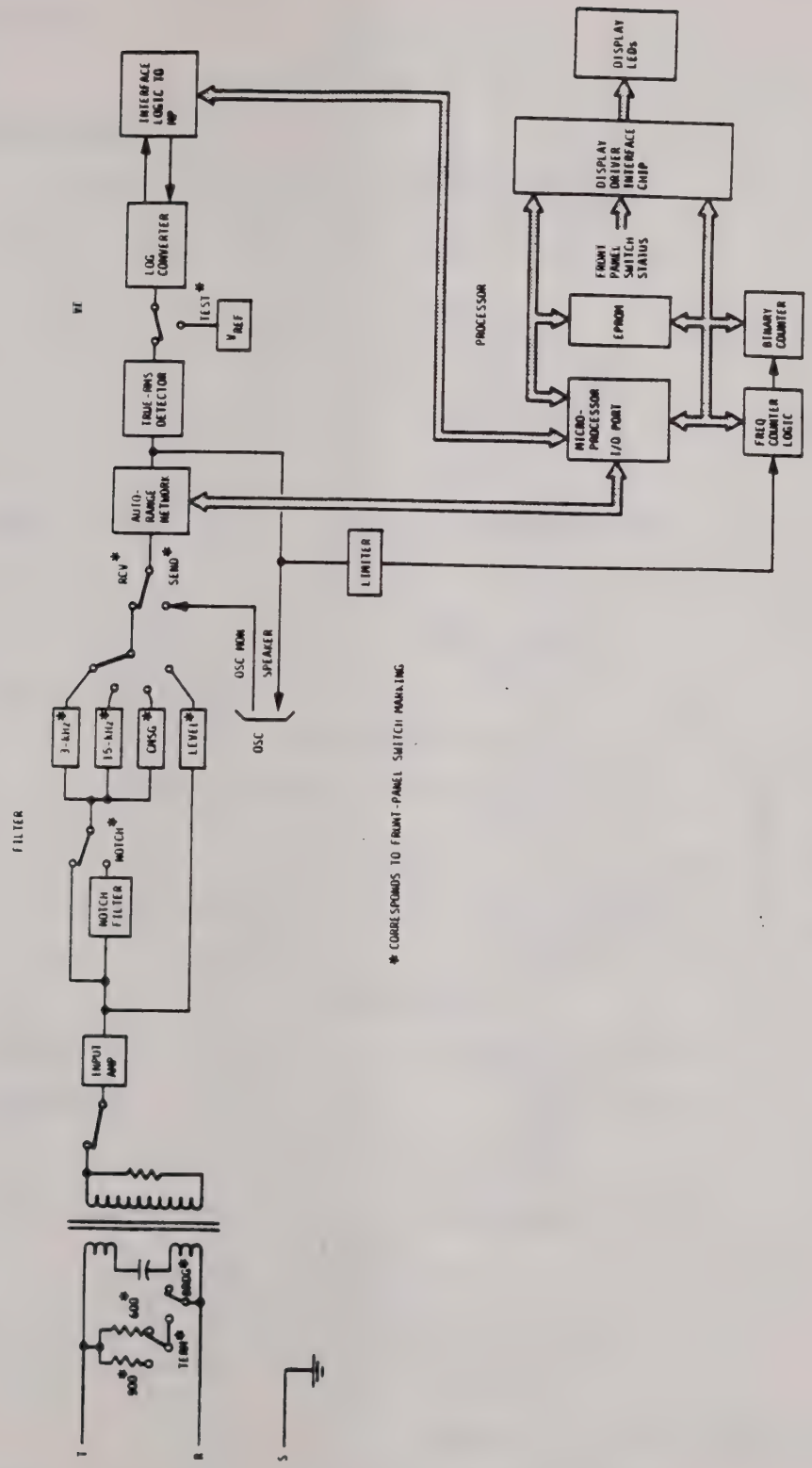


Fig. 9—Block Diagram (Sheet 1 of 2)

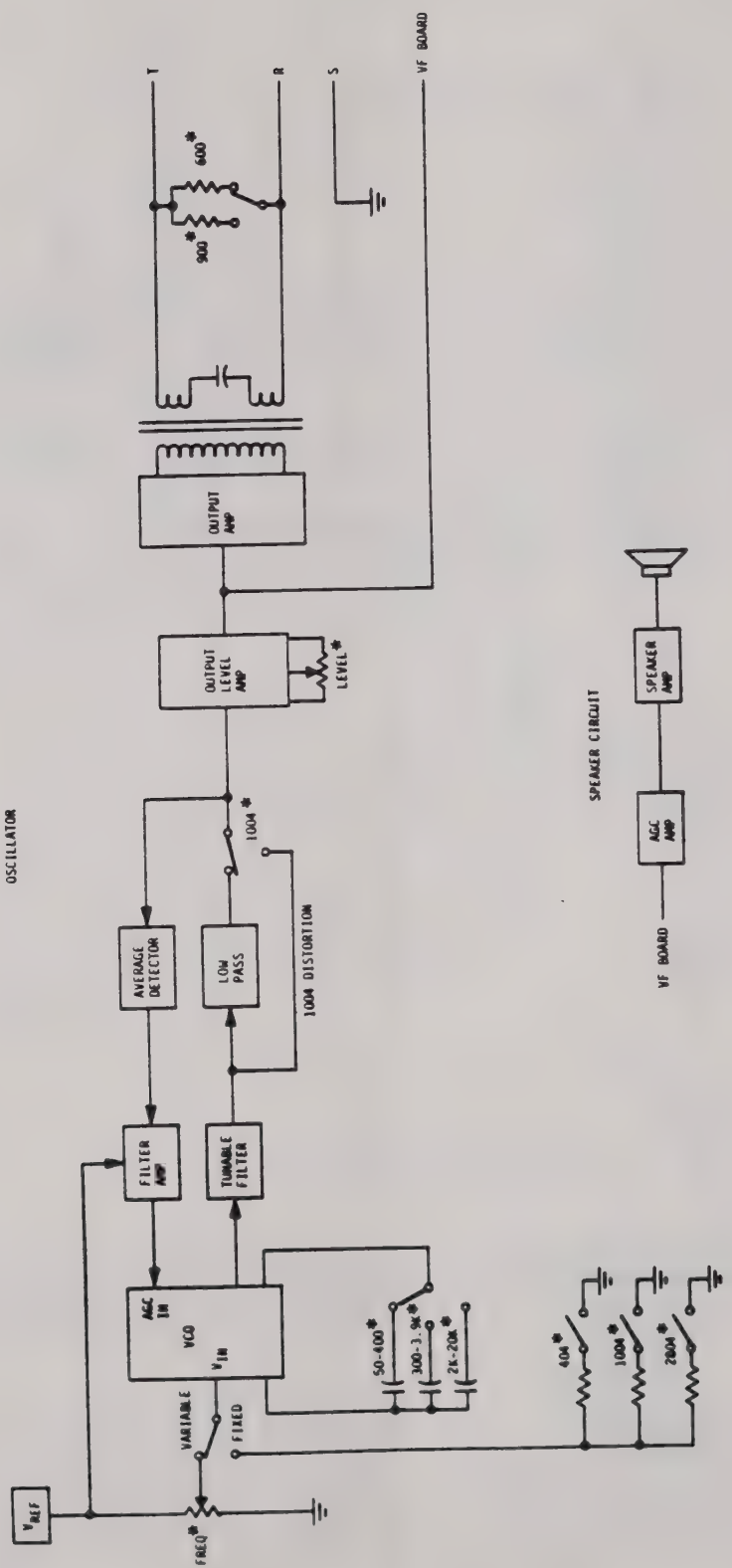


Fig. 9—Block Diagram (Sheet 2 of 2)

5. SPECIFICATIONS

A. Test Set

5.01 *Level Measurements*

Range ..... −49.9 to +10.4 dBm

Frequency ..... 50 Hz to 20 kHz

Resolution ..... 0.1 dB

Input Impedance..... 600 ohms

900 ohms

Bridging

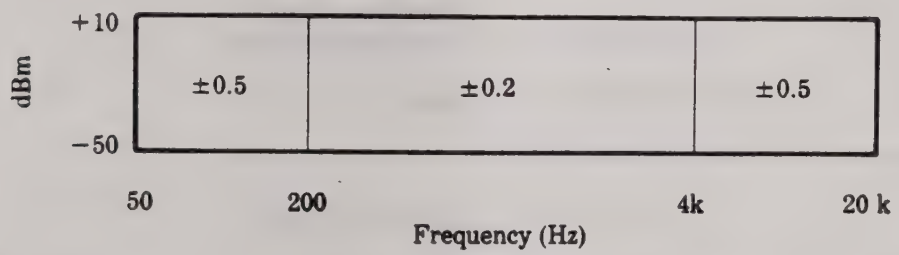
Longitudinal Balance..... 80 dB minimum at 60 Hz

Accuracy

1004 Hz..... 0.1 dB

All other frequencies and levels:

LEVEL ACCURACY



Temperature Coefficient..... ±0.005 dB/° C (0° to 50° C)

5.02 *Noise Measurements:*

Range ..... 10 to 100 dBrn

Frequency ..... 50 Hz to 20 kHz

Resolution ..... 1 dB

Input Impedance..... 600 ohms

900 ohms

Bridging

Longitudinal Balance..... 80 dB minimum at 60 Hz

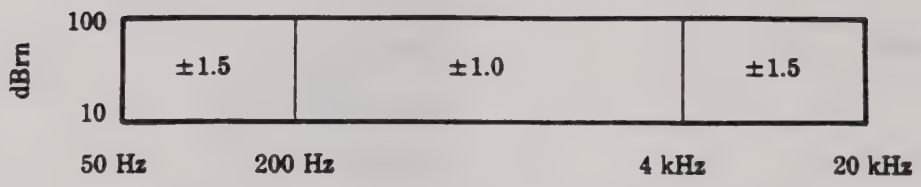
Weighting ..... C-message

3-kHz flat

15-kHz flat

Crest Factor ..... 10 dB minimum below 0.0 dBm (600 ohm reference)

NOISE ACCURACY



5.03 ***Frequency Measurements:***

Range ..... 50 Hz to 20 kHz

Level ..... −40 to +10 dBm

Resolution ..... 1 Hz below 10 kHz
10 Hz above 10 kHz

Accuracy 1Hz

5.04 ***Oscillator:***

Output Level ..... −40 to +10 dBm

Output Frequency

Low Band ..... 50 Hz to 400 Hz

Middle Band ..... 300 Hz to 3.9 kHz

High Band ..... 2 kHz to 20 kHz

Fixed ..... 404 Hz

1004 Hz

2804 Hz

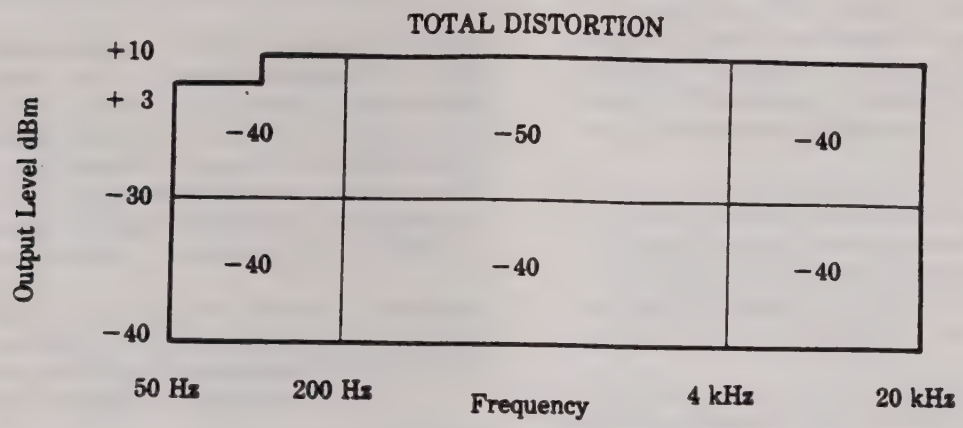
Output Impedance ..... 600 ohms (balanced)

900 ohms (balanced)

Total Distortion

1004 Hz..... −60 dB or better above −20 dBm

All other frequencies and levels:



5.06 ***Power Supply:***

Power Consumption.....8 watts

Voltage.....115 V ± 10%, 45-400 Hz

5.06 ***Mechanical:***

Height.....1.75 inches (4.5 cm)

Width .....17.25 inches (43.8 cm)

Depth .....10.50 inches (26.7 cm)

Weight .....7 lbs. (3.2 kg)

B. Remote Display

5.07 ***Power Characteristics:***

Power Consumption.....4.5 watts

Voltage.....−48 V dc ± 10%

5.08 ***Mechanical:***

Height.....11 inches (27.9 cm)

Width .....11 inches (27.9 cm)

Depth .....1.5 inches (3.8 cm)

Weight .....3 lbs. (1.4 kg)

WARNING

1. HANDLING STATIC-SENSITIVE DEVICES

1.01 Static-sensitive devices, such as circuit boards, can be adversely affected by static discharge. For example, a person in rubber-soled shoes walking across a tiled floor can produce a static charge of 10,000 volts or more. If he then comes in contact with a static-sensitive device, it can be easily affected, causing equipment failures such as electrical shorts, open circuits, or changes in critical values.

1.02 Since SAS equipment and many of our other products contain numerous static-sensitive devices, we recommend that several precautions be taken when handling our equipment:

(1) Carpeting on floors produces considerable static discharge, and you should avoid handling circuit boards in these areas.

(2) Handle circuit boards by the edges, being careful not to touch the pins or traces.

(3) When handling individual circuit board assemblies, wear static grounding wrist straps. If you do not have wrist straps, ground yourself by touching a chassis or unpainted rack support. When you are in transit with a board, protect it inside a conductive bag.

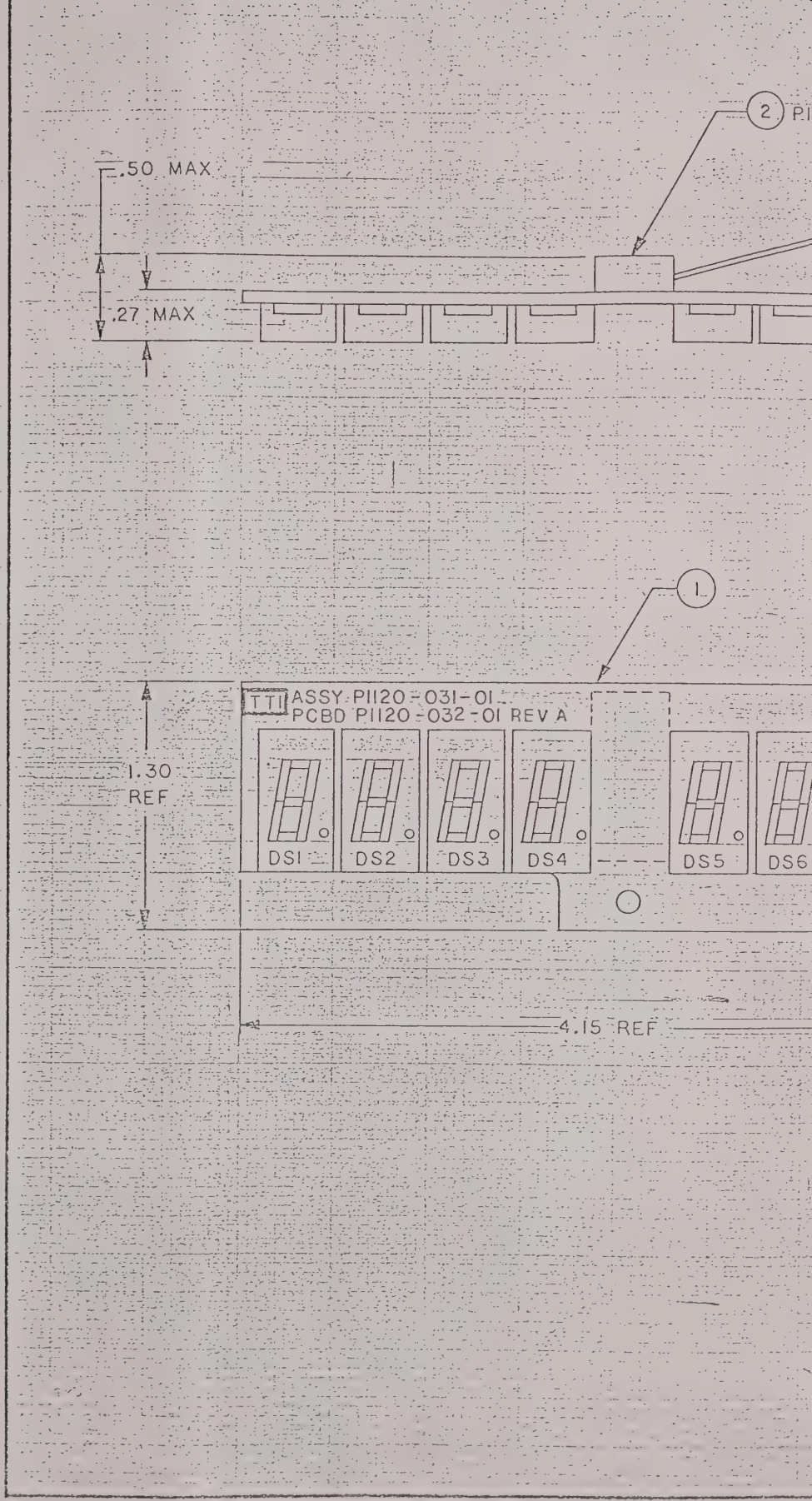
(4) If a circuit board must be returned to TTI, place it in an antistatic or a conductive bag. Avoid using packing material made of plastics such as polystyrene, polyethylene, or foam. We would be happy to supply you with the recommended bags if they are not available to you.

By following these few precautions, your equipment failures caused by electrostatic discharge will be minimized, and our service to you will be maximized.

Northeast Electronics
Regional Drive
P.O. Box 2021
Concord, NH 03301

1-603-228-3700

1-800-222-4124
(outside NH)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.27 MAX
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PCBD P1120-032-01 REV A
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REF
DS1
DS2
DS3
DS4
DS5
DS6
4.15 REF

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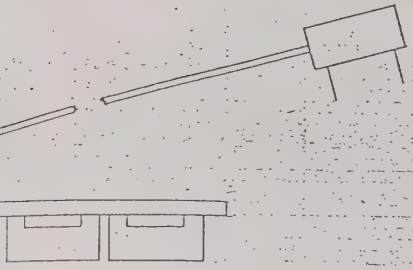
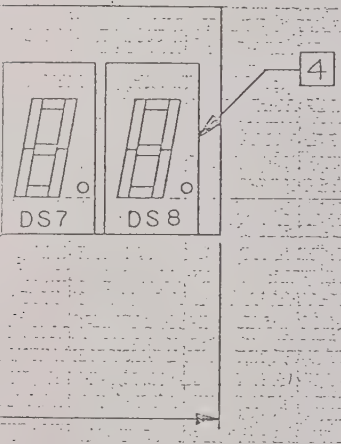
A	ECO 960 RELEASE ONLY	Paul J. 4-27-79	JC 1.2 5/21/79
B	ECO 1083	RS 8-8-79	KL 1.2 8/23/79
B	% ADD 1122B SA TO NEXT ASSY	RS 6-18-81	JC 6-18-81



NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE SCHEMATIC D-S1120-007-01
2. ASSEMBLE AND SOLDER PER TTI STANDARD PRACTICES AND PROCEDURES MANUAL
3. PRINTED CIRCUIT BOARD TO BE TESTED PER TTI SPECIFICATION
4. ALL DISPLAYS ON ANY INDIVIDUAL ASSY MUST HAVE THE SAME BRIGHTNESS LEVEL, EITHER ALL F's OR ALL G's.



SEE SEPARATE PARTS LIST
A-P1120-031-01

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
D-P1122-002-03	1	1	1122B SA
D-P1122-027-01	1	1	1122BE
D-P1120-002-03	1	1	1120E
D-P1120-002-02	1	1	1120DC
D-P1122-002-02	1	1	1122B
D-P1120-002-01	1	1	1120AC

TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX =±
•XXX =±
ANGULAR =±
SURFACE=

SCALE= 2/1

MATERIAL: SEE PARTS LIST

FINISH:

DWN. BY 4-11-78 J. Reyes
CHK. BY 4-19-78 J. Grand
APPD. BY 5/21/79 A. [illegible]

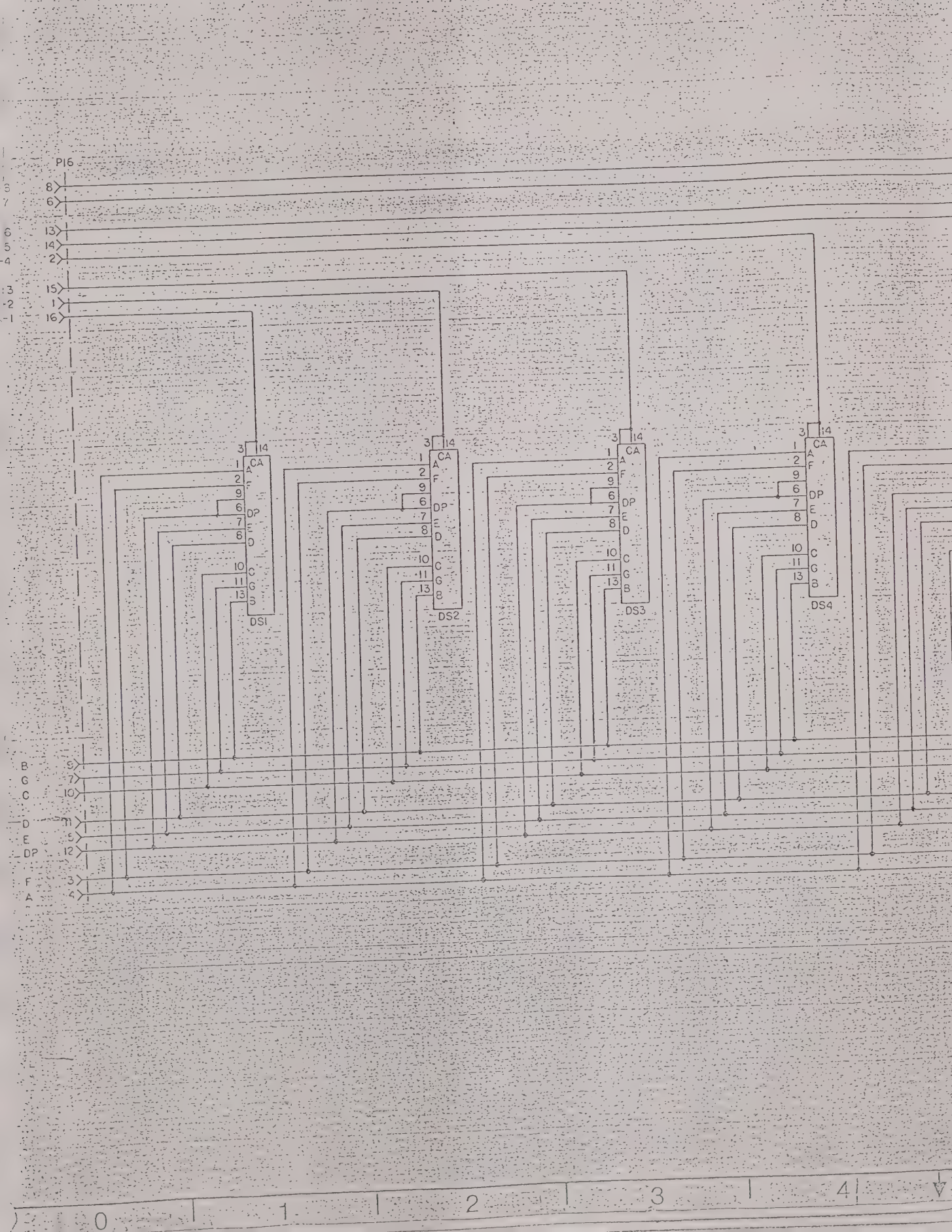
DRAWING TITLE: ASSEMBLY, DISPLAY BD

TELECOMMUNICATIONS TECHNOLOGY INC., 555 DEL REY, SUNNYVALE, CALIF. 94086 (408) 735-8080

C

DRAWING NUMBER: P1120-031-01

SHEET 1 OF 1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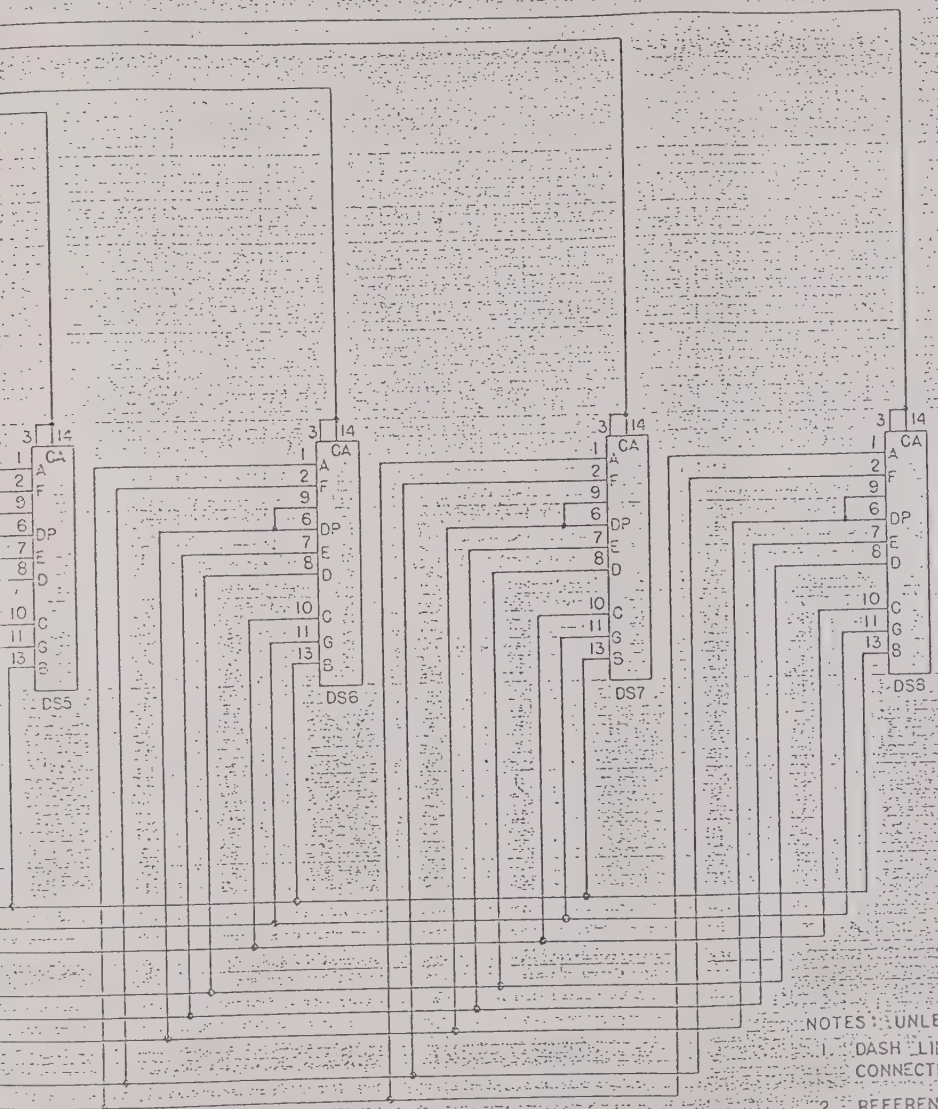

NOTES: UNLESS OTHERWISE SPECIFIED

1. DASH LINE (- - - -) DENOTES MECHANICAL CONNECTION OR MECHANICAL INTERLOCK.
2. REFERENCE PARTS LIST A-P1120-031-01

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
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.XXX =
ANGULAR =
SURFACE =
SCALE =

MATERIAL:
FINISH:

DWN. BY 10-4-77 TED TANAKA
CHK. BY 10-13-77 J. Clark
APPD. BY 1st shift

DRAWING TITLE: SCHEMATIC DISPLAY BOARD

C P1120-031-01			1120
NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER

TELECOMMUNICATIONS TECHNOLOGY INC., 555 DEL REY, SUNNYVALE, CALIF. 94086 (408) 735-8080

D

DRAWING NUMBER: S1120-007-01

SHEET OF

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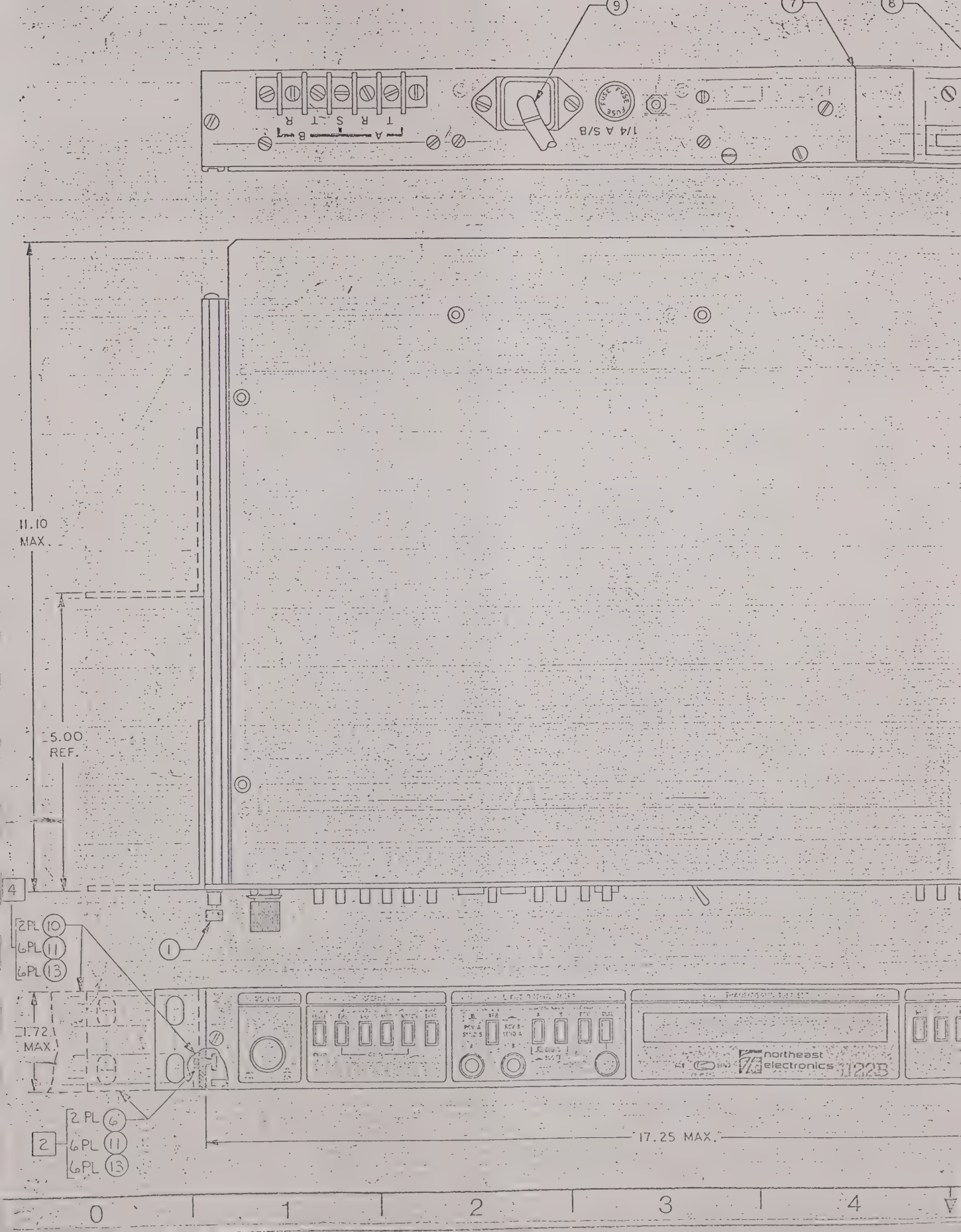
A B C D E F G H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7
8
T R S T R
A B
1/4 A S/B
FUSE
11.10
MAX.
5.00
REF.
4
2PL 10
6PL 11
6PL 13
1
11.72
MAX.
northeast
electronics
1223
2 PL 6
2
6PL 11
6PL 13
17.25 MAX.
0
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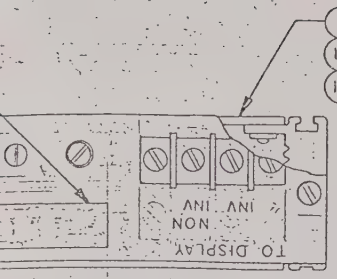
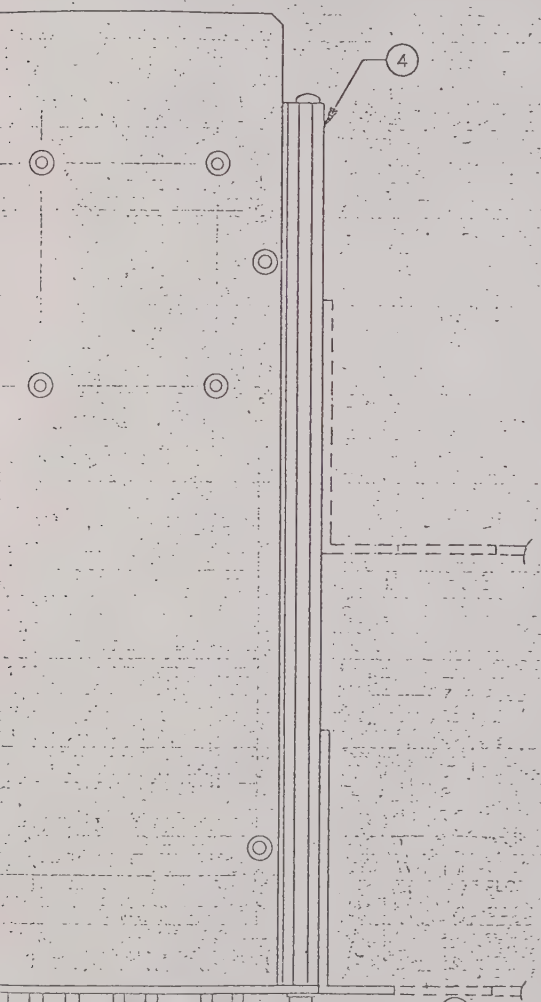
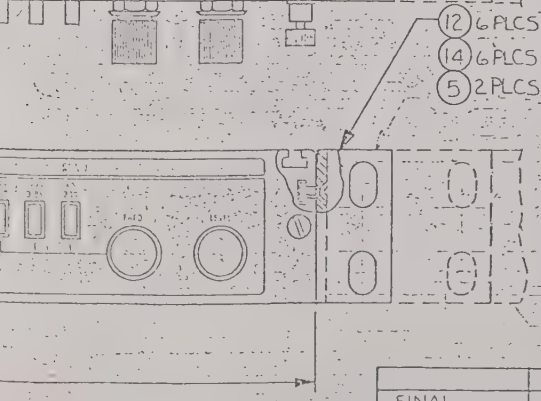
2
3
4

A	ECO 1087	RS 8-2-79	
B	ECO 1270	RS 1-29-80	
C	ECO 1326	RS 7-19-80	
D	ECO 1683	RS 6-9-81	
E	ECO 2315	MK. 8-15-83	
F	ECO 88-55		







NOTES:

1. REFERENCE INTERCONNECT DIAGRAM T1122-003-01
2. USE ON 23" RACK, OPTIONAL.
3. USE ON 19" RACK, OPTIONAL.
4. USE ON 30 1/2" RACK, OPTIONAL.
5. MFG: BELDEN CORP., CODE IDENT NO. 16428.
6. MFG: SOUTHCO INC., CODE IDENT NO. 94222
7. ASSEMBLE PER STANDARD PRACTICES AND PROCEDURES MANUAL.
8. ASSEMBLY TO BE TESTED PER SPECIFICATION.
9.
10. ITEM 17 TO BE SHIPPED WITH UNIT

SEE SEPARATE BILLS OF MATL A112200102

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
.XX = ±
.XXX = ±
ANGULAR = °
SURFACE =

MATERIAL:
FINISH:
DRAWING TITLE: ASSEMBLY, TRANSMISSION TEST SET
SCALE = 1/1

DWN. BY 8-2-79 E Sinden
CHK. BY 8/10/79
APPD. BY 8/31/79

NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
FINAL	—	—	1122B

northeast electronics

D

DRAWING NUMBER: A112200102 DEV F

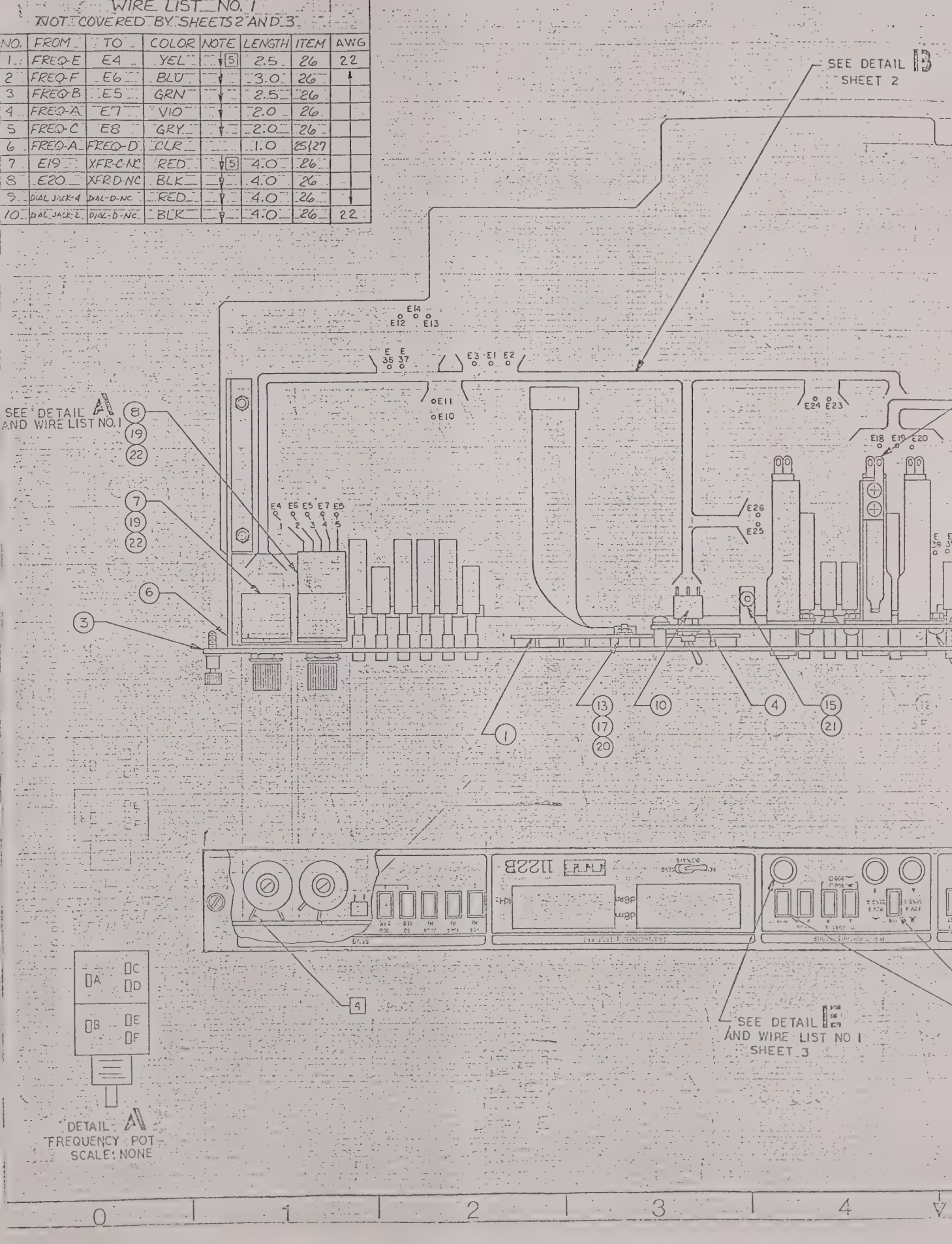
SHEET 1 OF 1

5 6 7 8 9

A B C D E F G H

WIRE LIST NO. 1
NOT COVERED BY SHEETS 2 AND 3

NO.	FROM	TO	COLOR	NOTE	LENGTH	ITEM	AWG
1	FREQ-E	E4	YEL	[5]	2.5	26	22
2	FREQ-F	E6	BLU		3.0	26	
3	FREQ-B	E5	GRN		2.5	26	
4	FREQ-A	E7	VIO		2.0	26	
5	FREQ-C	E8	GRY		2.0	26	
6	FREQ-A	FREQ-D	CLR		1.0	25|27	
7	E19	XFR-C-NC	RED	[5]	4.0	26	
8	E20	XFR-D-NC	BLK		4.0	26	
9	DIAL JACK-4	DIAL-D-NC	RED		4.0	26	
10	DIAL JACK-2	DIAL-D-NC	BLK		4.0	26	22



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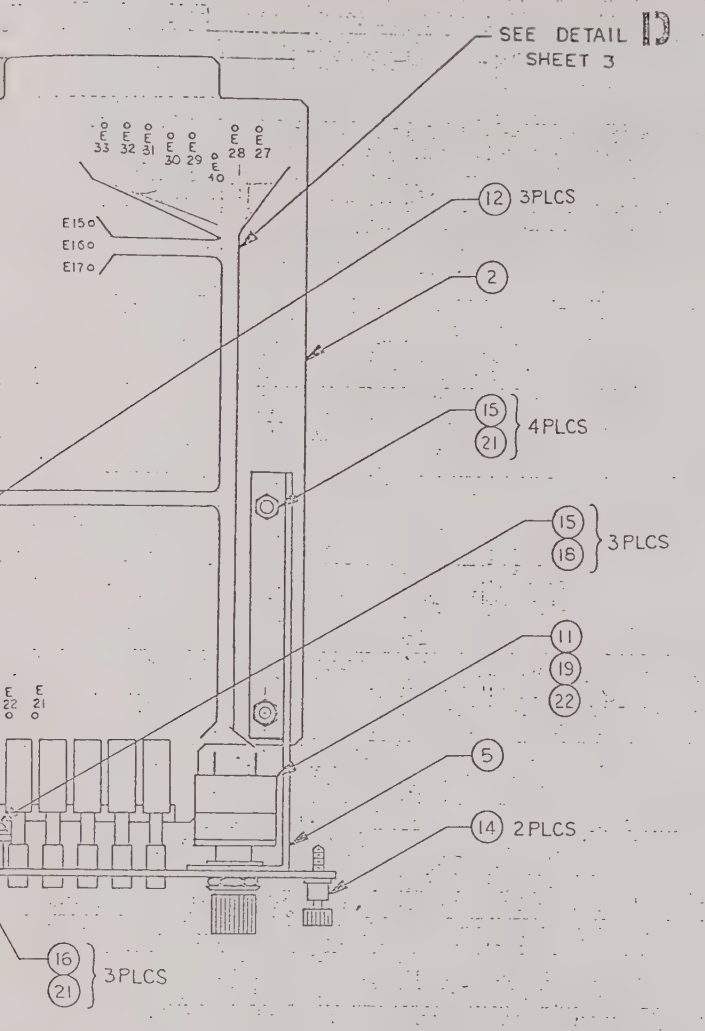
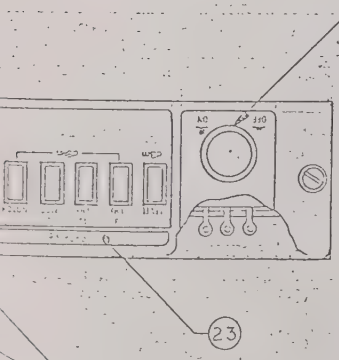
B	ECO 1236	RS 1-3-80	[illegible]
C	ECO 1286	RS 2-13-80	[illegible]
D	ECO 1463	RS 9-18-80	[illegible]
E	ECO 1507	KN. 12-8-80	[illegible]
F	ECO 1593	RS 4-28-81	[illegible]
G	ECO 1773	JD 9-22-81	[illegible]
H	ECO 1929	SD 3/25/82	[illegible]





NOTES:

1. SEE SHEETS 2 AND 3 FOR WIRING HARNESS.
2. ASSEMBLE AND SOLDER PER STANDARD PRACTICES AND PROCEDURES MANUAL.
3. ASSEMBLY TO BE TESTED PER TTI SPECIFICATION.
4. POSITION PINS ON POTS EXACTLY AS SHOWN.
5. ↓ DENOTES TWISTED WIRES.

SEE SEPARATE PARTS LIST
A-P1122-002-02

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX = ±
•XXX = ±
ANGULAR = ±
SURFACE =

MATERIAL:
FINISH:
DRAWING TITLE: ASSEMBLY, FRONT PANEL

DWN. BY 5-2-79
CHK. BY 5/9/79
APPD. BY 10/31/79

SCALE = 1/1

NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
D-P1122-001-02	1	1	1122E

TELECOMMUNICATIONS TECHNOLOGY INC.
555 DEL REY,
SUNNYVALE, CALIF. 94086 (408) 735-8080

D

DRAWING NUMBER: P1122-002-02

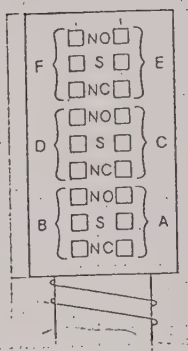
SHEET 1 OF 3

5 6 7 8 9

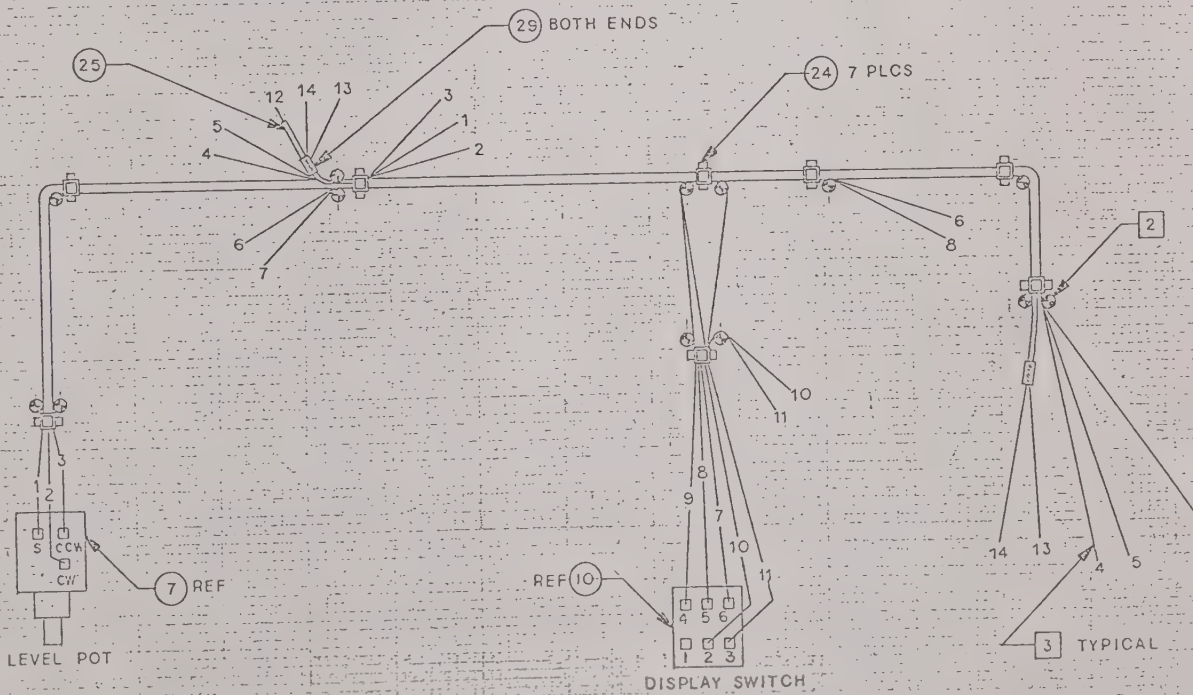
A B C D E F G H

WIRE LIST NO. 2 FOR DETAIL B

NO.	FROM	TO	COLOR	NOTE	LENGTH	ITEM	AWG
1	E1	LEVEL-S	BRN	[5]	9.5	26	22
2	E2	LEVEL-CW	RED		9.5	26	
3	E3	LEVEL-CCW	ORN		9.5	26	
4	E37	E38	BLK	[5]	13.5	26	
5	E36	E39	WHT/BLK		13.5	26	
6	E11	E24	BLK	[5]	10.5	26	
7	E10	DIS SW-6	WHT		10.0	26	
8	E23	DIS SW-5	WHT/YEL		9.0	26	
9	E21	DIS SW-4	WHT/ORN		13.0	26	
10	E26	DIS SW-2	WHT/GRY	[5]	3.5	26	
11	E25	DIS SW-3	WHT/RED		3.5	26	22
12	E12	GRD	SLEEVE		14.0	25&28	
13	E13	XFR-C-NO	RED				
14	E14	XFR-D-NO	BLK				



DETAIL C
PUSHBUTTON SWITCHES
SCALE - NONE



DETAIL B
PART OF P1122-002-02

ISSUE	CHANGE ORDER NUMBER	DRAWN BY: DATE:	APPROVED BY: DATE:
A	ECO 1087	RS 10-19-79	
B	ECO 1236	RS 1-3-80	
C	ECO 1286	RS 2-13-80	
D	ECO 1463	RS 9-18-80	
E	ECO 1507	K.N. 12-8-80	
F	ECO 1593	RS 4-28-81	
G	ECO 1773	JD 9-22-81	
H	ECO 1929	JD 3/24/82	

NOTES:

1. REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY AND MAY NOT APPEAR ON PART.
2. ⊙ DENOTES POSITIONS FOR NAILS TO ASSIST IN WIRING OF HARNESS.
3. SEE SHEET 1 FOR FINAL SOLDERING OF FLYING LEADS.
4. ENDS TO BE STRIPPED AND TINNED .30"
5. DENOTES TWISTED WIRES.

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
.X = ±.2
.XX = ±.—
.XXX = ±.—
ANGULAR = ±.—
SURFACE = —

MATERIAL:

FINISH:

DWN. BY 10-19-79 L. Singer
CHK. BY
APPD. BY

DRAWING TITLE: ASSEMBLY, FRONT PANEL

SCALE = —

NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
	—	—	11228

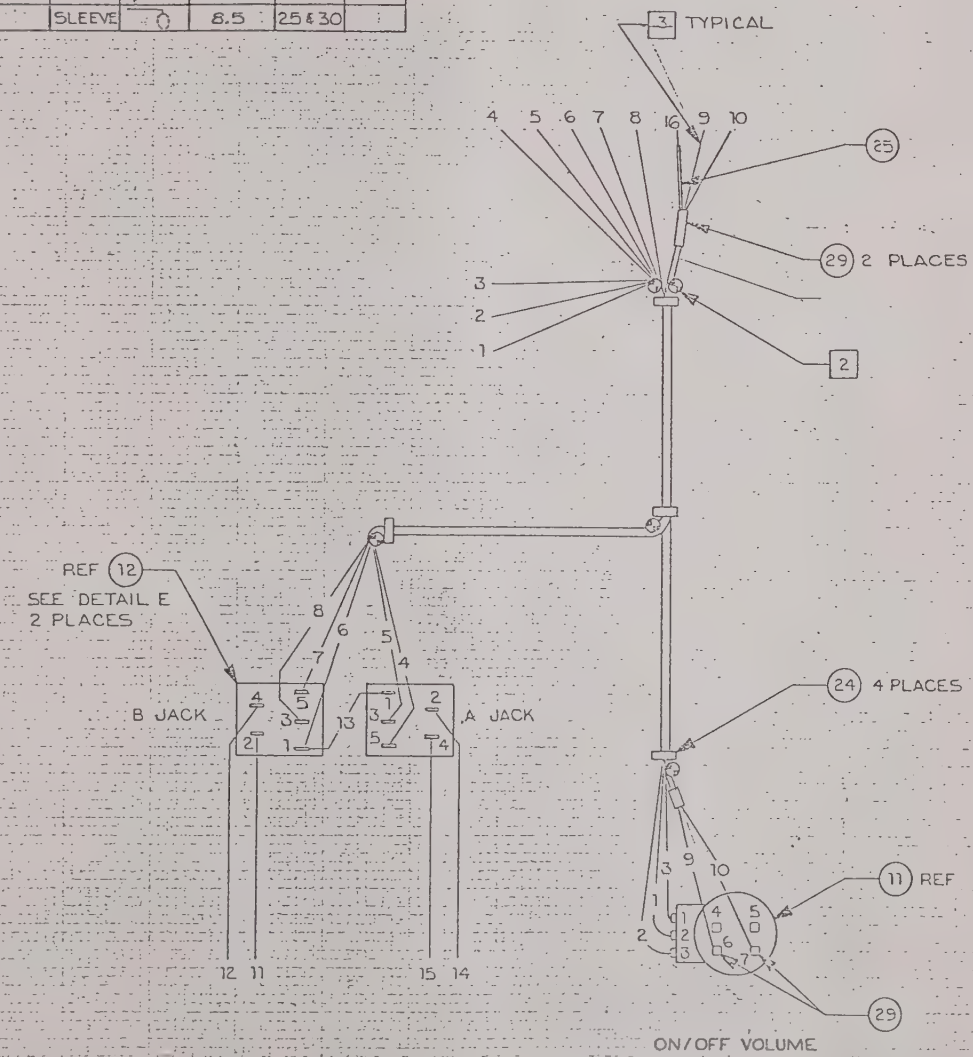
TELECOMMUNICATIONS TECHNOLOGY INC.,
555 DEL REY,
SUNNYVALE, CALIF. 94086 (408) 735-8080

D

DRAWING NUMBER: P1122-002-02

SHEET 2 OF 3

NO.	FROM	TO	COLOR	NOTE	LENGTH	ITEM	AWG
1	E17	ON/OFF VOL-2	WHT/VIO	[5]	8.5	26	22
2	E16	ON/OFF VOL-3	WHT/BLU		8.5	26	
3	E15	ON/OFF VOL-1	WHT/GRN		8.5	26	
4	E33	A JACK-5	GRAY	[5]	8.5	26	
5	E32	A JACK-3	WHT		8.5	26	
6	E31	B JACK-1	BLU		8.5	26	
7	E30	B JACK-5	GRN		8.5	26	
8	E29	B JACK-3	VIO		8.5	26	
9	E28	ON/OFF VOL-6	RED		8.5	30	
10	E27	ON/OFF VOL-7	BLK		8.5	30	
11	B JACK-2	DIAL-C-S	BLK	[5]	3.5	26	
12	B JACK-4	DIAL-D-S	RED		3.5	26	
13	A JACK-1	B JACK-1	CLR		1.0	25,27	
14	A JACK-2	XFR-D-S	BLK	[5]	3.5	26	
15	A JACK-4	XFR-C-S	RED		3.5	26	22
16	E40		SLEEVE		8.5	25&30	



DETAIL - D

PART OF P1122-002-02

ISSUE	CHANGE ORDER NUMBER	DRAWN BY: DATE:	APPROVED BY: DATE:
A	ECO 1087	AGM 10/22/79	
B	ECO 1236	RS 1-3-80	
C	ECO 1286	RS 2-13-80	
D	ECO 1463	RS 9-18-80	
E	ECO 1507	KN 12-8-80	
F	ECO 1593	RS 4-28-81	
G	ECO 1773	JD 9-22-81	
H	ECO 1929	JD 3/26/82	

NOTES: UNLESS OTHERWISE SPECIFIED

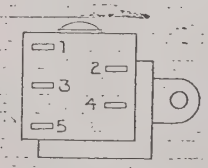
1. REFERENCE DESIGNATIONS ARE FOR REFERENCE ONLY AND MAY NOT APPEAR ON PART.

[2] ⊕ DENOTES POSITIONS FOR NAILS TO ASSIST IN WIRING OF HARNESS.

[3] SEE SHEET 1 FOR FINAL SOLDERING OF FLYING LEADS.

4. ENDS TO BE STRIPPED AND TINNED .30.

[5] DENOTES TWISTED WIRES.



DETAIL – [F]
JACKS
SCALE: NONE

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
.X = ±.2
.XX = ± —
.XXX = ± —
ANGULAR = ± —
SURFACE = ——

MATERIAL:
FINISH:
DRAWING TITLE: ASSEMBLY, FRONT PANEL
SCALE = 1:1

DWN. BY 10-19-79 AGM
CHK. BY
APPD. BY

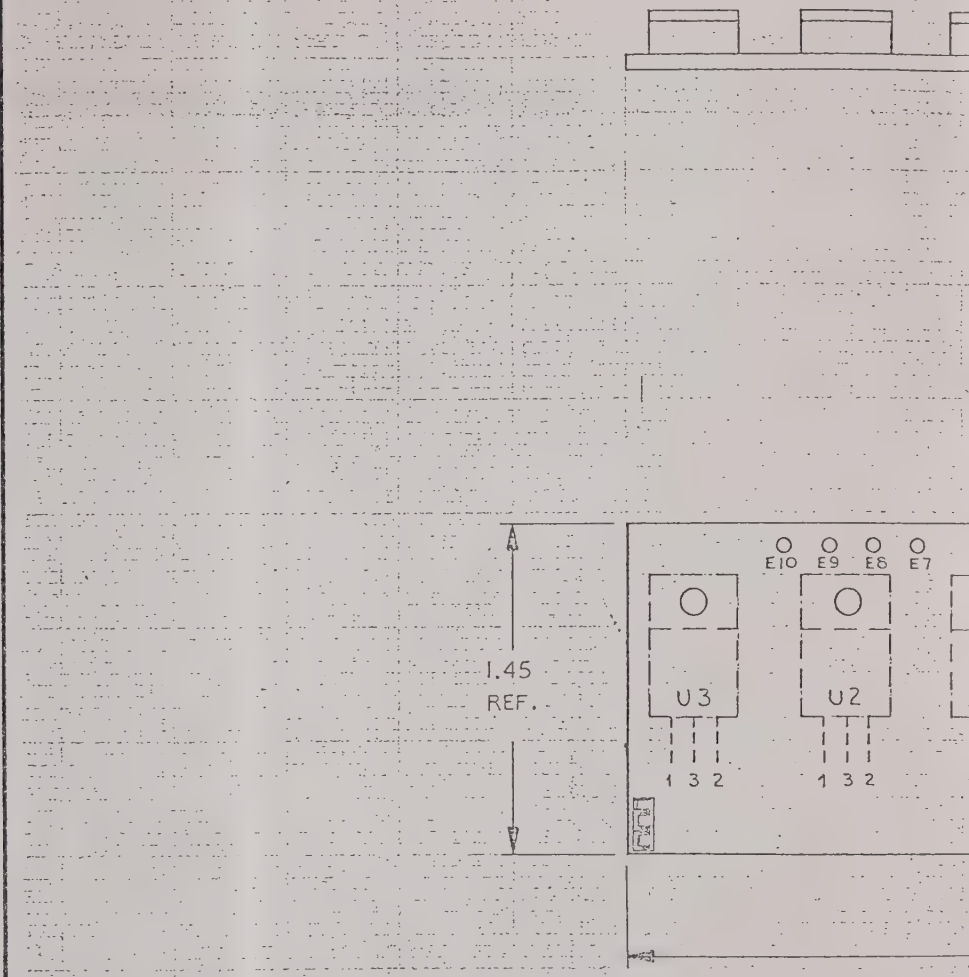
NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
			11228

TELECOMMUNICATIONS TECHNOLOGY INC., 555 DEL REY, SUNNYVALE, CALIF. 94086 (408) 735-8050

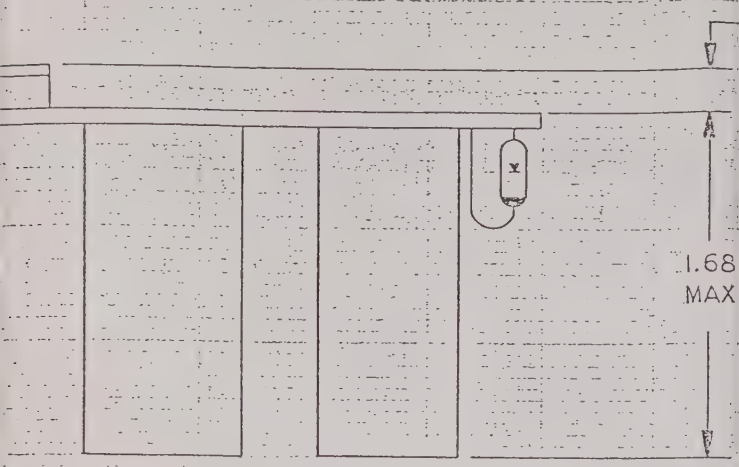
D

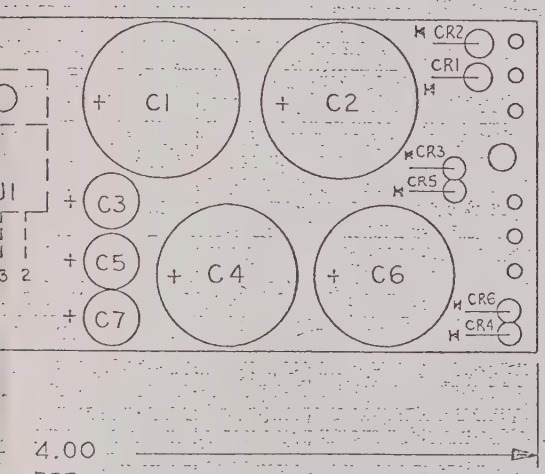
DRAWING NUMBER: P1122-002-02

SHEET 3 OF 3


E10
E9
E8
E7
U3
U2
1 3 2
1 3 2
1.45
REF.

ISSUE	CHANGE ORDER NUMBER	DRAWN BY: DATE:	APPROVED BY: DATE:
A	ECO 782	Hao 9-21-78	JC 10-30-78
B	ECO 1245	ES 1-3-80	JC-A 1





NOTES:

1. REFERENCE SCHEMATIC C-S1122-003-01
2. ASSEMBLE AND SOLDER PER TTI STD. PRACTICES AND PROCEDURES MANUAL.
3. BOARD TO BE TESTED PER TTI SPECIFICATION.

SEE SEPARATE PARTS LISTS
A-P1122-015-01

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX = ±
•XXX = ±
ANGULAR = ±
SURFACE =

MATERIAL:

FINISH:

DWN. BY Nguyen 9-21-78
CHK. BY J. Chan 1/30/78
APPD. BY A. [illegible] 1/11/80

DRAWING TITLE: ASSEMBLY POWER SUPPLY BD

SCALE= 2/1

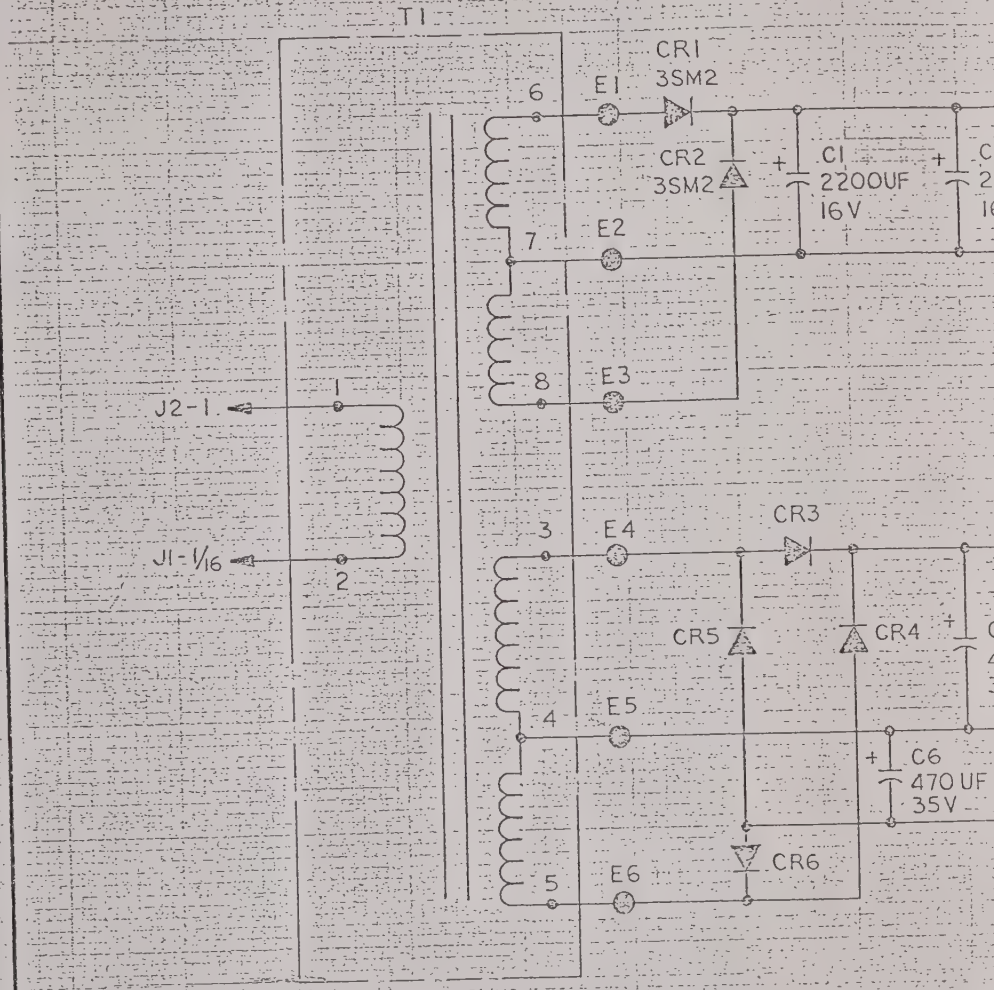
NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
D-P1122-014-01	1	1	1122B

TELECOMMUNICATIONS TECHNOLOGY INC., 555 DEL REY, SUNNYVALE, CALIF. 94086 (408) 735-8080

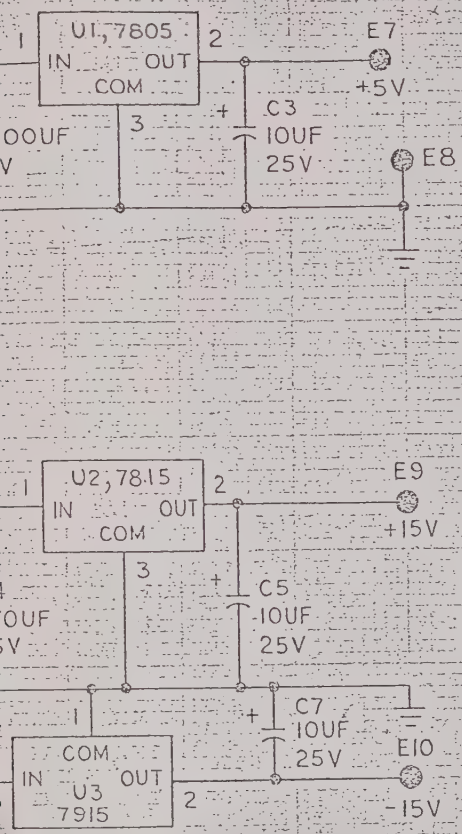
C

DRAWING NUMBER: P1122-015-01

SHEET 1 OF 1


T1
CR1
3SM2
E1
6
CR2
3SM2
C1
2200UF
16V
7
E2
8
E3
J2-1
1
J1-1/16
2
3
E4
CR3
CR5
CR4
4
E5
C6
470 UF
35V
CR6
5
E6

ISSUE	CHANGE ORDER NUMBER	DATE:	DATE:
A	ECO 782	Hao 10/11/78	ZC 10-30-78
B	ECO 1245	RS 1-3-80	ZC 1-8



NOTES: UNLESS OTHERWISE SPECIFIED

1. CAPACITOR VALUES ARE EXPRESSED IN MICROFARADS.
2. ALL DIODES ARE IN4001
3. PHANTOM LINE (— — —) DENOTES A COMPONENT MOUNTED OFF THE PCB
4. REFERENCE PARTS LIST A-P1122-015-01

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX =±
•XXX =±
ANGULAR =±
SURFACE=

MATERIAL:

FINISH:

DWN. BY Nguyen Tao 10-11-78
CHK. BY T. Chan 10/30/78
APPD. BY A.G. 1/11/80

DRAWING TITLE: SCHEMATIC
POWER SUPPLY BD

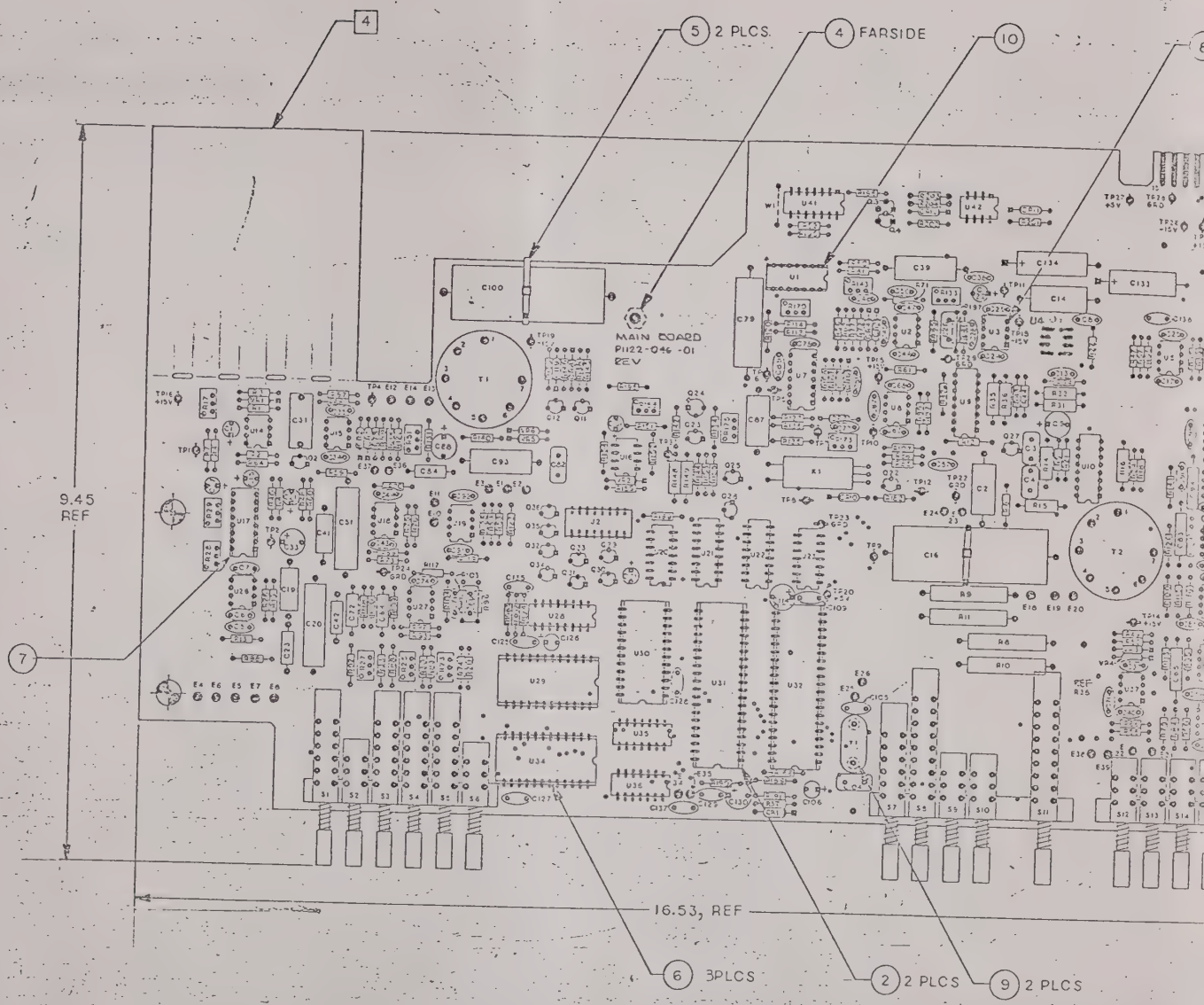
SCALE= NONE

C-P1122-015-01	REF	REF	1122B
NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER

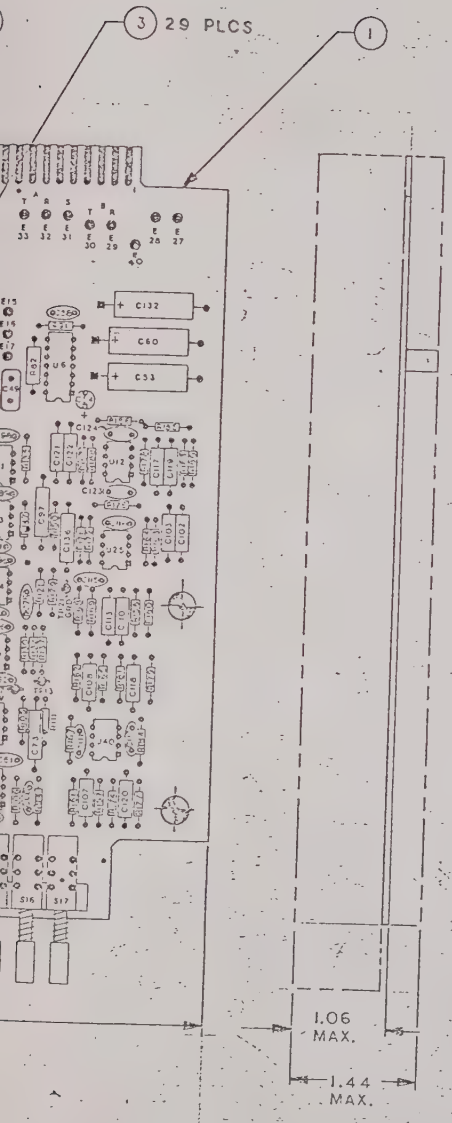
TELECOMMUNICATIONS TECHNOLOGY INC.,
555 DEL REY,
SUNNYVALE, CALIF. 94086 (408) 735-8080

C | DRAWING NUMBER: S1122-003-01

SHEET 1 OF 1


4
5 2 PLCS.
4 FARSIDE
10
MAIN BOARD
P1122-046-01
REV
9.45
REF
7
16.53, REF
6 3PLCS
2 2 PLCS
9 2 PLCS

ISSUE	CHANGE ORDER NUMBER	DRAWN BY: DATE:	APPROVED BY: DATE:
A	ECO 1507	KN. 12/8/20	
B	ECO 1695	GS 6-29-81	
C	ECO 1773	JD 9-22-81	
D	ECO 1803	JD 10-23-81	
E	ECO 2025	JD 7-13-82	
F	ECO 2137	LN 1-11-83	
G	ECO 2214	JD 8/11/83	
H	ECO 2592	TPL 5/16/84	
J	ECO E2782	TPL 10/10/84	
K	ECO 3386	IBB 11/13/85	
L	ECO 3874	IBB 9-30-86	
M	ECO 87-93	JW 6/11/87	
N	CHANGED VALUES OF C49, C84 AND R47. ECO # 87-283.		3-11-88
P	ECO 88-37 REV CHANGE		3-27-88
R	BIM CHNG ECO 88-138 JW		
S	BIM CHNG ECO 88-142 JW		
T	BIM CHNG ECO 88-145 JW		



NOTES:

1. REFERENCE SCHEMATIC NO. A112200701
2. ASSEMBLE AND SOLDER PER NE STANDARD PRACTICES AND PROCEDURES MANUAL.
3. P.C. BOARD TO BE TESTED PER NE SPECIFICATION.
4. TAB TO BE REMOVED AFTER WAVE SOLDER HAS BEEN COMPLETED.
5. USE DRAWING A112220900 FOR 19"
   USE DRAWING A112220901 FOR 23"

SEE SEPARATE BILL OF MATL A112204601

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE
A	112200202	1	11228 SA	
SEE NOTE 5		1	11223	
NEXT ASSEMBLY		NEXT QTY.	FINAL QTY.	MODEL NUMBER

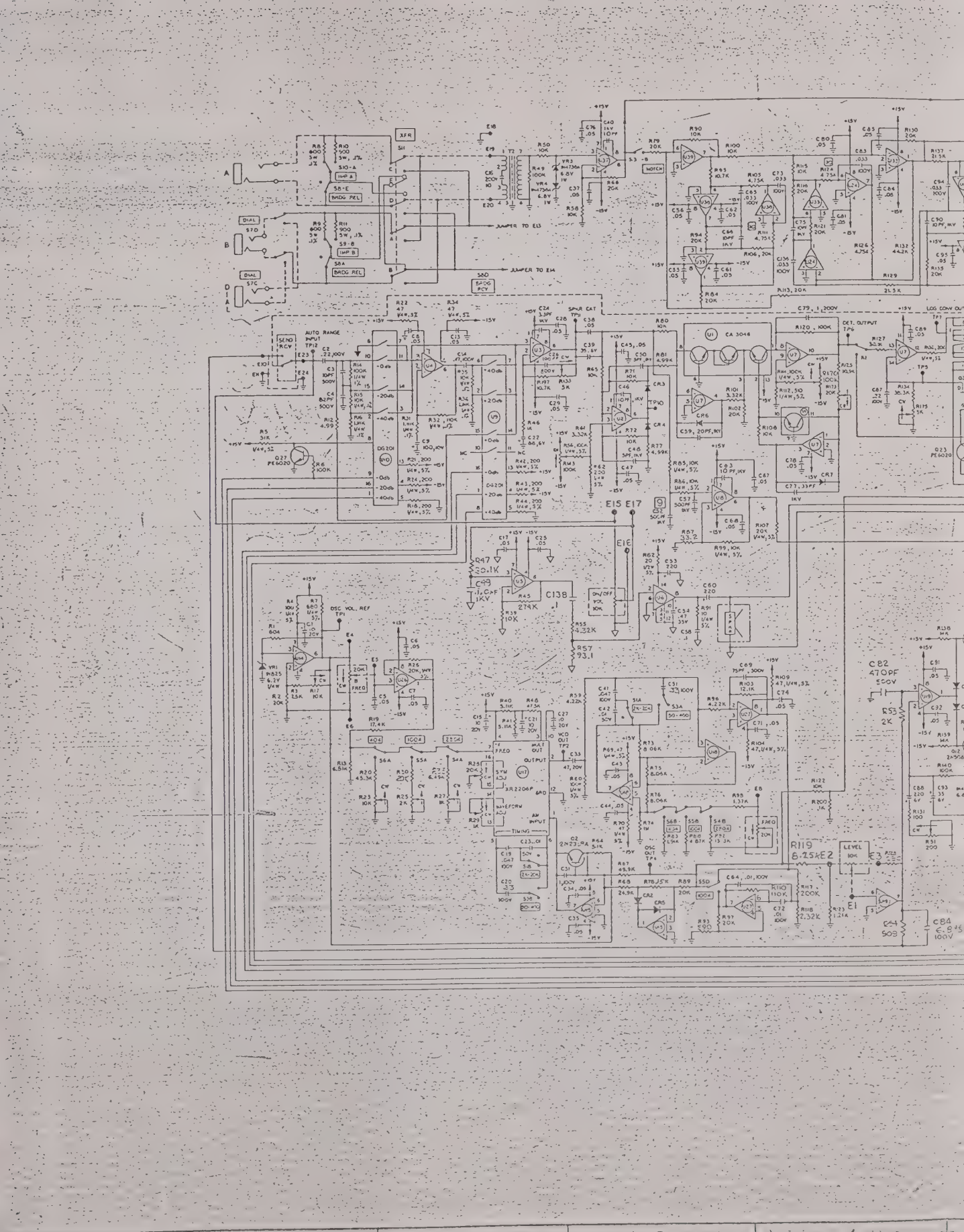
TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX =±
•XXX =±
ANGULAR =±
SURFACE=
SCALE= 1/1

MATERIAL:
FINISH:
DRAWING TITLE: ASSEMBLY MAIN BOARD (DOM)

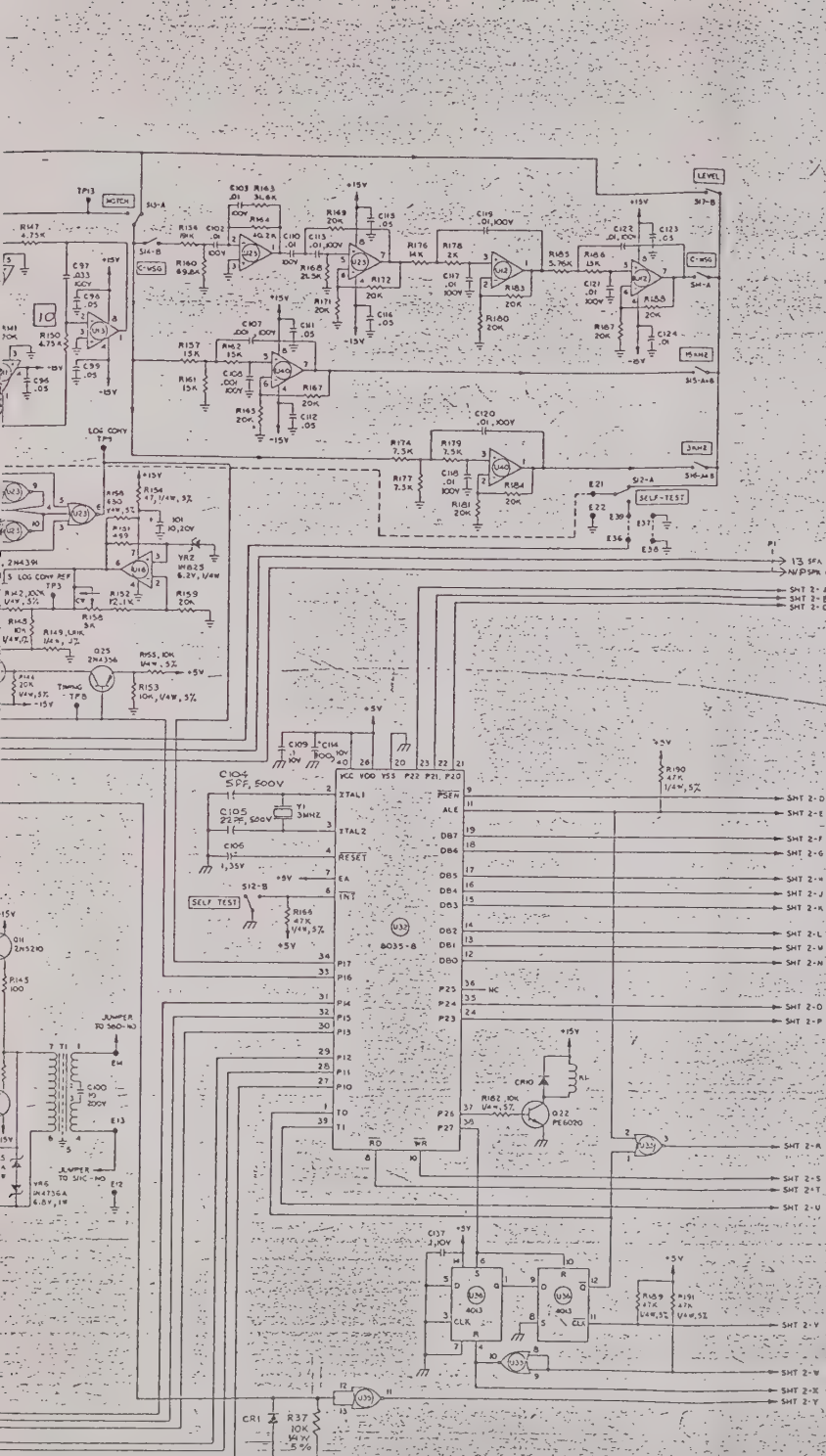
DWN. BY 12/8/20
CHK. BY 12/11/80
APPD. BY GW 12-12-80

northeast electronics

D
DRAWING NUMBER A112204601
REV. T


CA 3046
XR2206P
2N2923A
DG201
DG202
AUTO RANGE
OSC VOL. REF
OSC OUT TP4
LOG CONV OUT
DET. OUTPUT
LEVEL
JUMPER TO E13
JUMPER TO E14
NOTCH
XFR
SEND RCV
DIAL
BRDG REL
BRDG RCV
R119 8.25kE2
E15 E17
E16
E1
E2
E3
E4
E5
E6
E8

ISSUE	CHANGE ORDER NUMBER	DRAWN BY: DATE:	APPROVED BY: DATE:
A	ECO 1507	KN 12/10/80	
B	ECO 1773	JD 9-22-81	
C	ECO 1803	JD 10-23-81	
D	ECO 2025	JD 7-13-82	KMT 8-11-82
E	ECO 2137	LN 1-11-83	KMT 2-24-83
F	ECO 2782	RK 10/10/84	12/18/84
G	ECO 33386	IBM 11/5/85	12/2, 12/4/
H	CHANGED VALUE OF C49, C84 AND R47 PER ECO 87-283		
J	CHANGE VALUE R78, 119 ECO 88-145		RCOR



NOTES: UNLESS OTHERWISE SPECIFIED

1. RESISTOR VALUES ARE EXPRESSED IN OHMS, 1/8W, 1%.
2. CAPACITOR VALUES ARE EXPRESSED IN MICROFARADS, 25V.
3. ALL DIODES ARE IN4148.
4. ALL TRANSISTORS ARE MPS-A12.
5. HEAVIER LINE DENOTES THE PRIMARY SIGNAL.
6. PHANTOM LINE (— — —) DENOTES A COMPONENT MOUNTED OFF THE PCB.
7. REFERENCE PARTS LIST A-P1122-046-01.
8. DASH LINE (----) DENOTES MECHANICAL CONNECTION OR MECHANICAL INTERLOCK.

[9] NOT LOADED.

[10] FACTORY SELECT VALUES FOR RESISTORS OTHER THAN 4.81KΩ ARE 4.75KΩ OR 4.87KΩ AS IS CURRENT.

ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
.XX = ± —
.XXX = ± —
ANGULAR = ± —
SURFACE = —

MATERIAL:
FINISH:
SCALE = NONE

DWN. BY 12/10/80 KA Nelson
CHK. BY 12/10/80
APP'D. BY 8-11-82

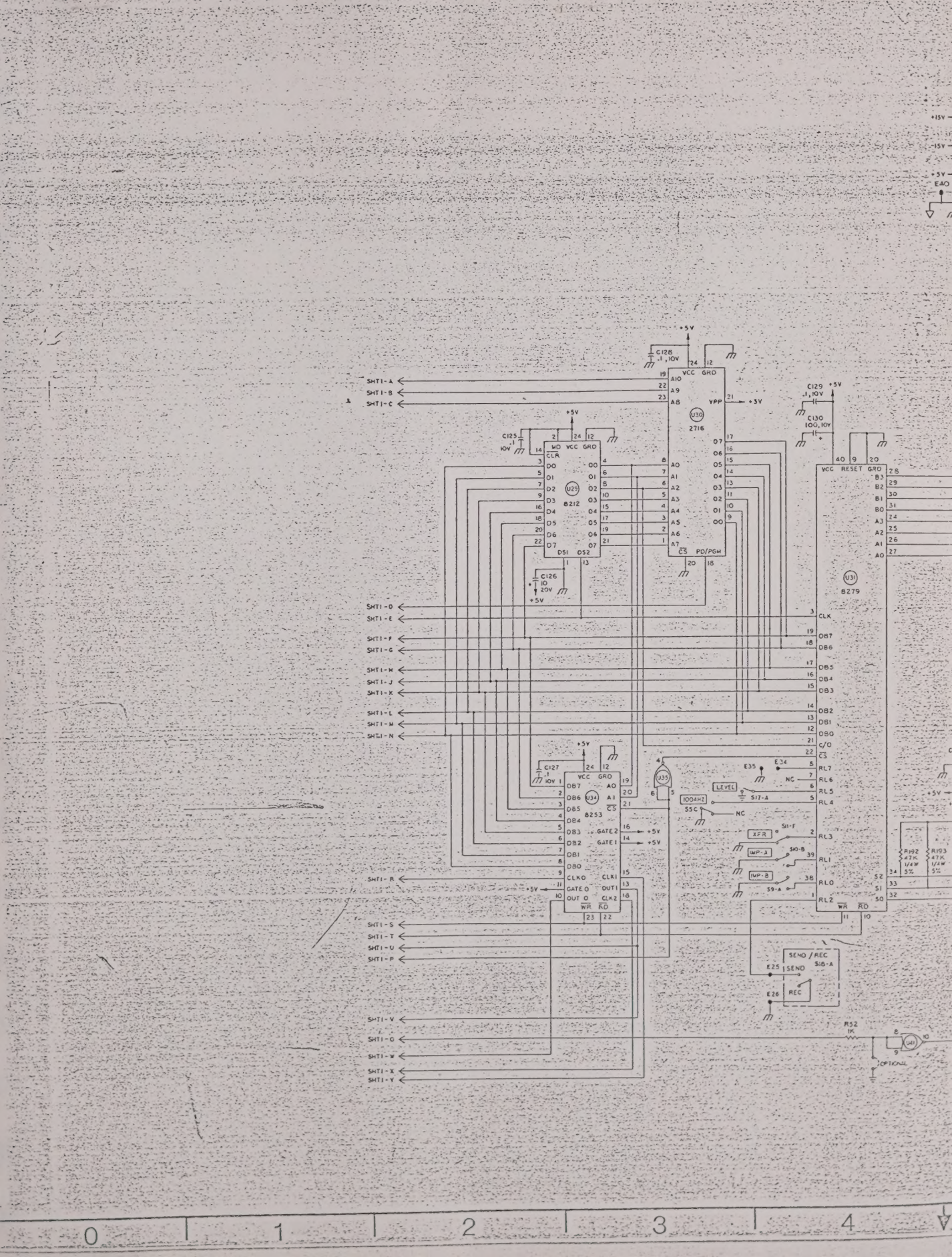
DRAWING TITLE: SCHEMATIC MAIN BOARD (DOM)

A1122-046-01	REF	REF	11225
NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER

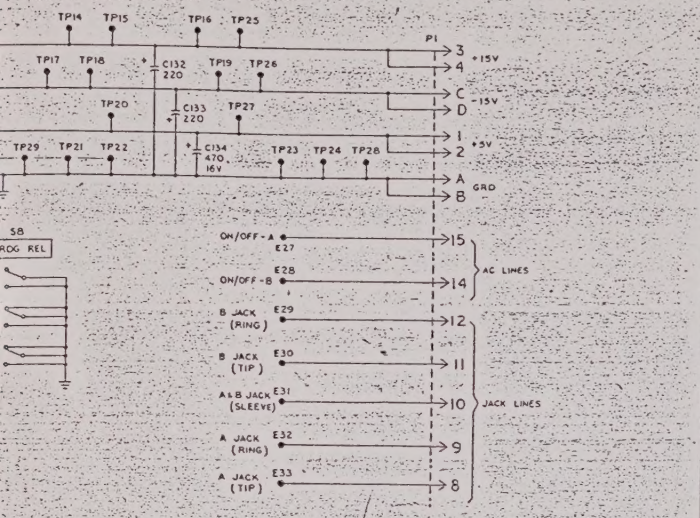
northeast electronics — A UNIT OF GENERAL SIGNAL

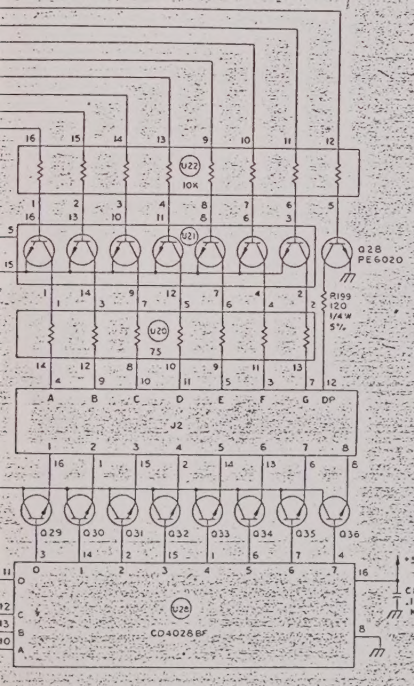
D | DRAWING NUMBER: R1122-007-01 | REV J

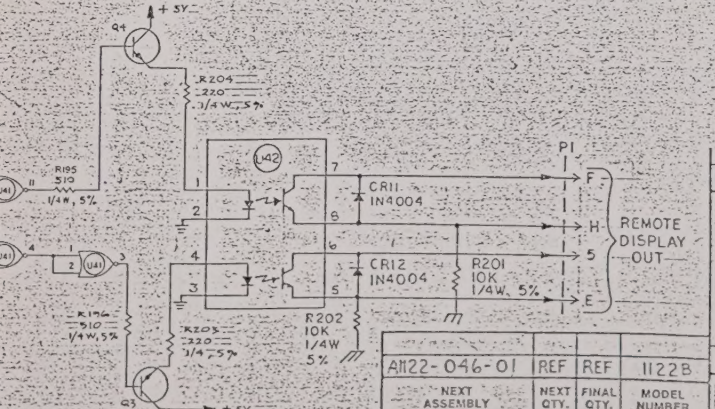
SHEET 1 OF 2


+15V
-15V
+5V
E40
SHTI-A
SHTI-B
SHTI-C
SHTI-D
SHTI-E
SHTI-F
SHTI-G
SHTI-H
SHTI-J
SHTI-K
SHTI-L
SHTI-M
SHTI-N
SHTI-R
SHTI-S
SHTI-T
SHTI-U
SHTI-P
SHTI-V
SHTI-Q
SHTI-W
SHTI-X
SHTI-Y
C128 .1, 10V
C129 .1, 10V
C130 100, 10V
C125 .1 10V
C126 10 20V
C127 .1 10V
U30 2716
U29 8212
U31 8279
U34 8253
U35
VCC GRD
VPP
CS PD/PGM
WD CLR
DS1 DS2
RESET
CLK
DB7
DB6
DB5
DB4
DB3
DB2
DB1
DB0
C/D
WR RD
GATE2
GATE1
CLK0 CLK1
GATE 0 OUT1
OUT 0 CLK2
RL7
RL6
RL5
RL4
RL3
RL1
RL0
RL2
E34
E35
NC
LEVEL
S17-A
1004HZ
S5C
XFR
S11-F
IMP-A
S10-B
IMP-B
S9-A
SEND / REC
S16-A
E25
SEND
E26
REC
R192 47K 1/4W 5%
R193 47K 1/4W 5%
R52 1K
OPTIONAL

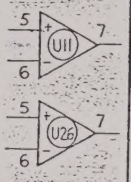
ISSUE	NUMBER	DATE:	DATE:
A	ECO 1507	K.N 12/10/80	12/12/80
B	ECO 1773	JD 9-22-81	10/13/81
C	ECO 1803	JD 10-23-81	11/6/81
D	ECO 2025	JD 7-13-82	KM Twain 8-11-82
E	ECO 2137	LN 1-11-83	KM Twain 1-25-83
F	ECO 2782	PK 10-10-84	10/9/84
G	ECO 3306	LB33 11/15/85	11/4/
H	SEE SHEET 1		
	SEE SHEET 1		







IC TABLE				REF. DES.		SPARE GATES
REF. DES.	TYPE	VSS	VDD	LAST USED	NOT USED	
U1	3046			C138	C30, 32	
U37	748			CR12	C36, 18	
U4	HA2525-5			K1	Q5-Q10	
U5, 14, 16	741			J2	Q13-Q21	
U6	LM380			Q36	Q26, Q1	
U7	4136			R204		
U9, U10	DG201			S17	R63	
U11-13, 18, 24-27, 33	4558			T2		
U38-40	4558			U42		
U15, 19	TL082			VR6		
U17	XP2206P			Y1		
U20	899-3			E40	E9	
U21	3081			TP29		
U22	898-3					
U23	4025					
U28	4028					
U29	8212					
U30	2716					
U31	8279					
U32	8035-8					
U34	8253					
U35, 41	4001	7	14			
U36	4013					
U3	TL081					
U42	MCT-6	—	—			
U8, U2	TL080	—	—			



ITEM	PART NUMBER	QTY.	DESCRIPTION	NOTE

TOLERANCE UNLESS OTHERWISE SPECIFIED
•XX = ± —
•XXX = ± —
ANGULAR = ± —
SURFACE = —

MATERIAL:

FINISH:

DRAWING TITLE: SCHEMATIC MAIN BOARD (DOM)

SCALE = NONE

DWN. BY 12/10/80 K A N
CHK. BY
APPD. BY

NEXT ASSEMBLY	NEXT QTY.	FINAL QTY.	MODEL NUMBER
AM22-046-01	REF	REF	1122B

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D | DRAWING NUMBER: R1122-007 01 | REV J

SHEET 2 OF 2

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